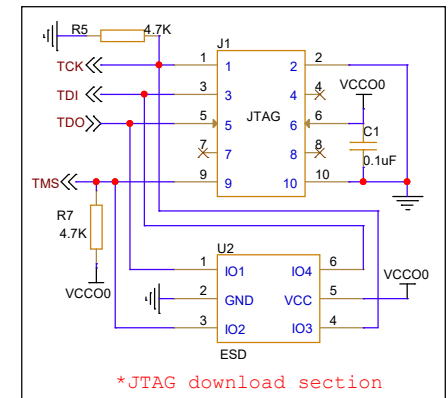
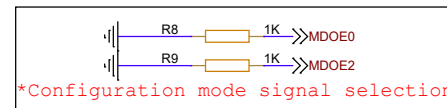
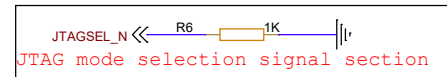
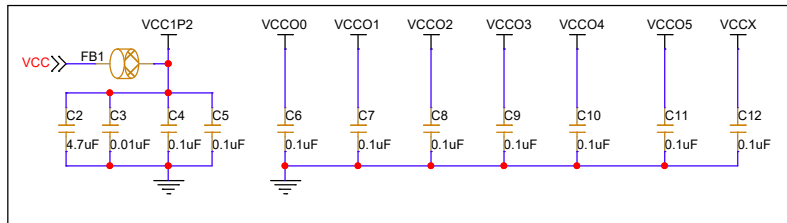
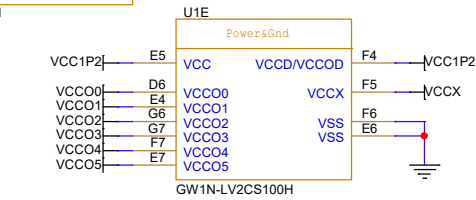
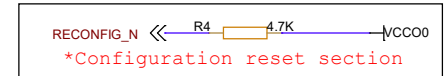
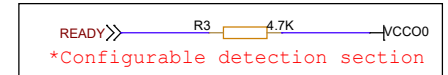
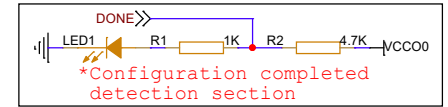
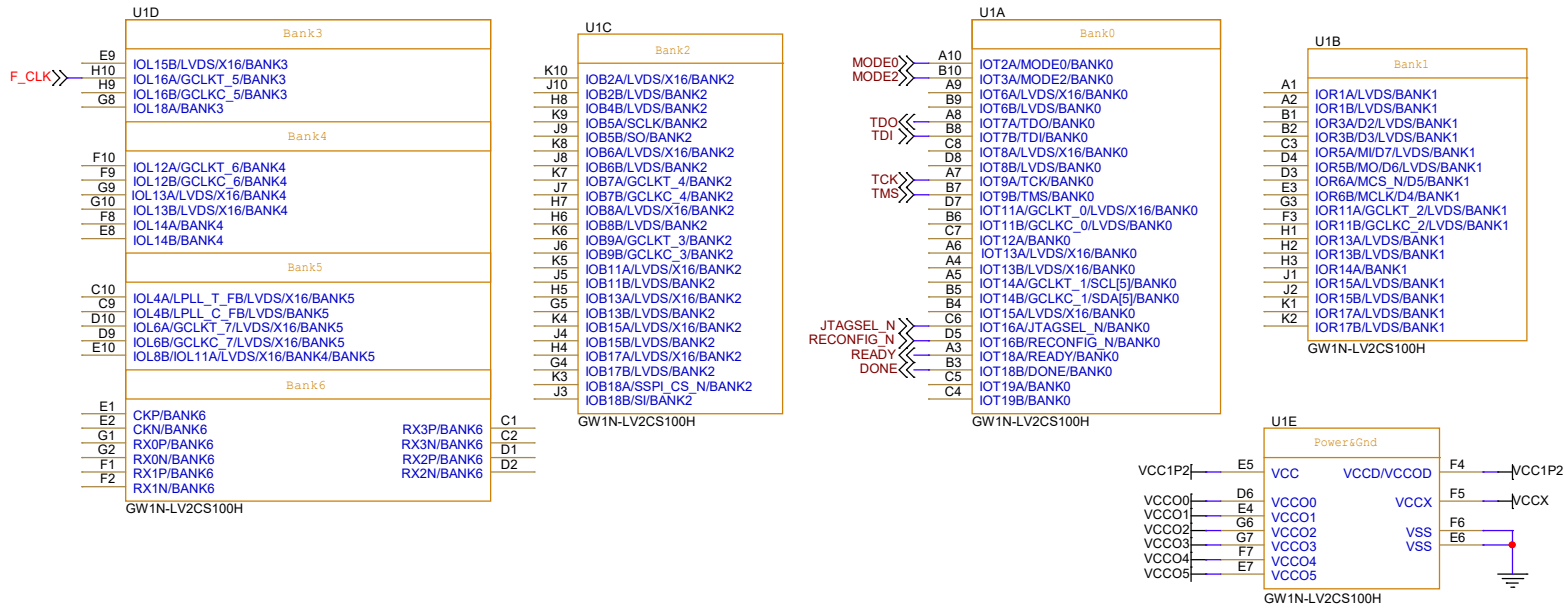


GW1N-LV2CS100H

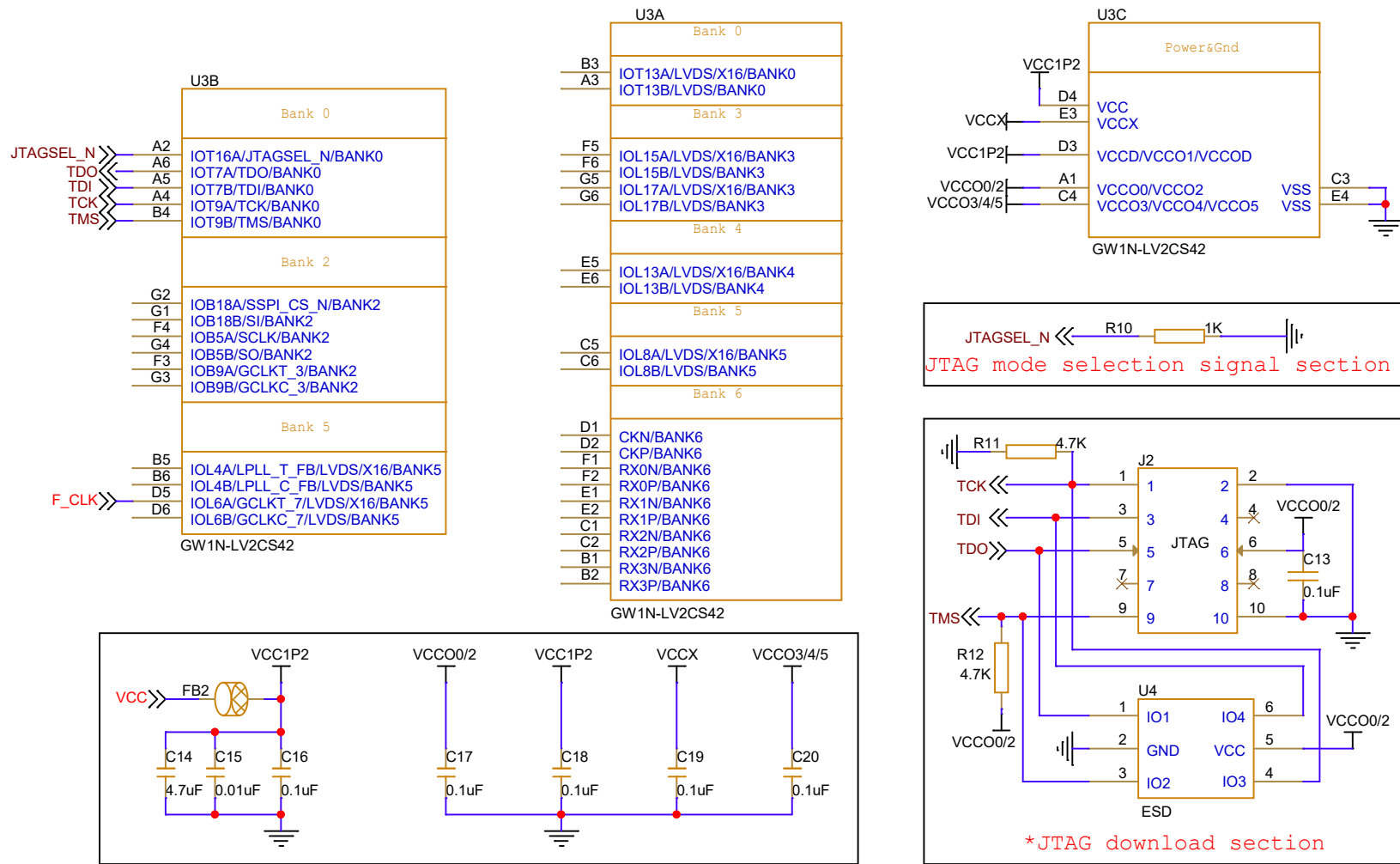


Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

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GOWIN Minimum System SCH		
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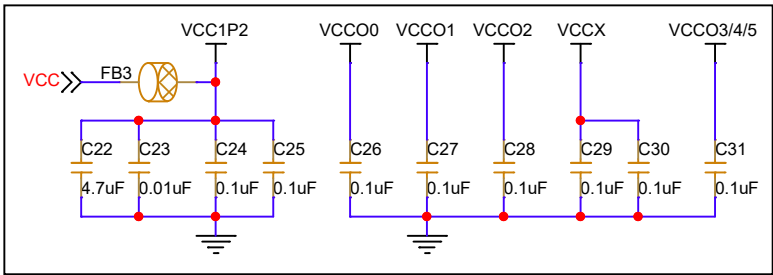
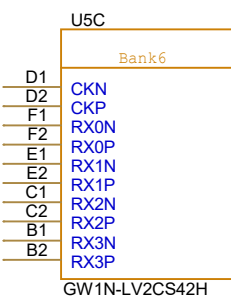
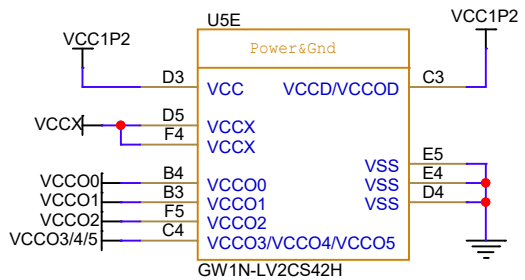
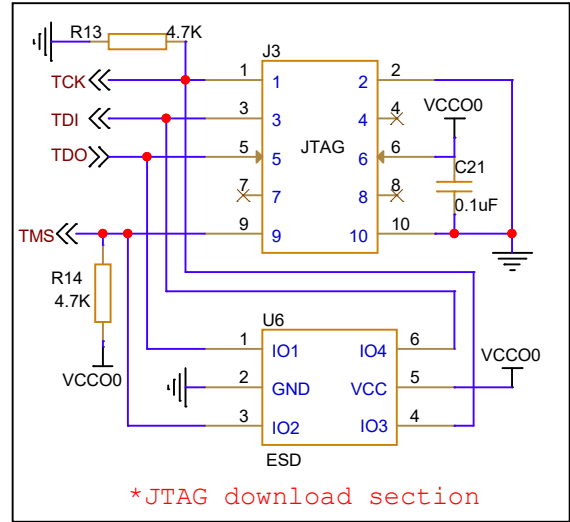
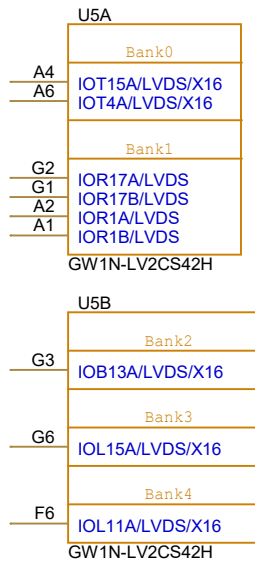
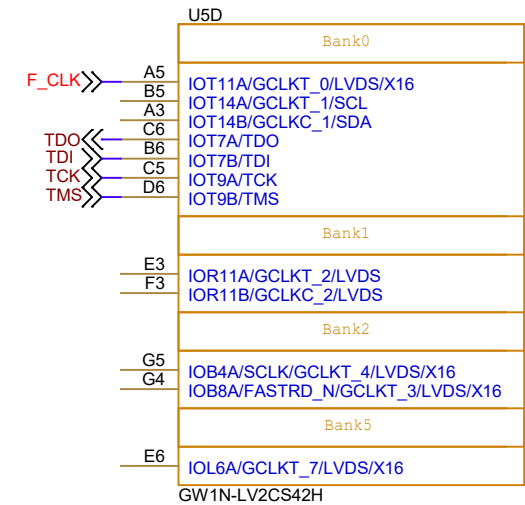
GW1N-LV2CS42



Notes:

- 1.F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

GW1N-LV2CS42H



- Notes:
- 1.F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
 - 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

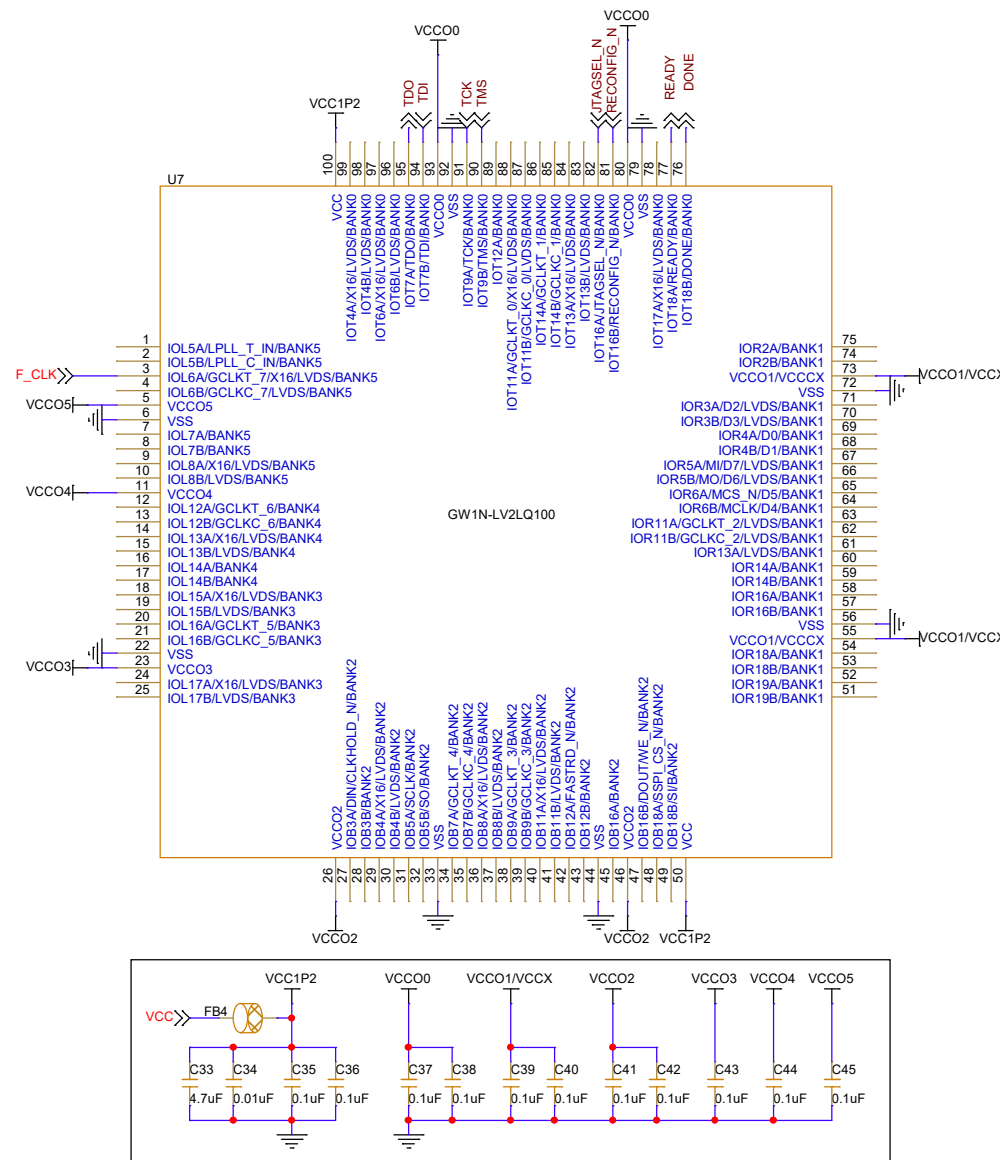
Title
GOWIN Minimum System SCH

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A4

Document Number
GW1N-LV2CS42H

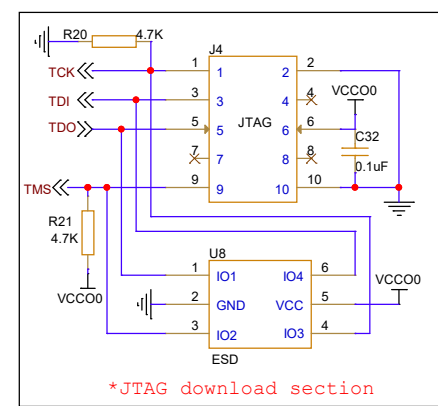
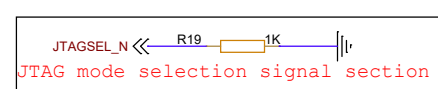
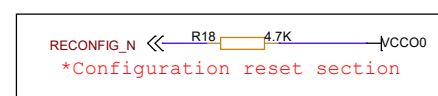
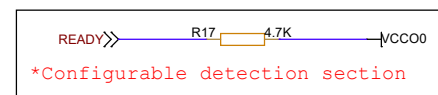
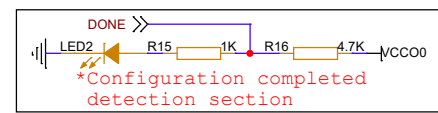
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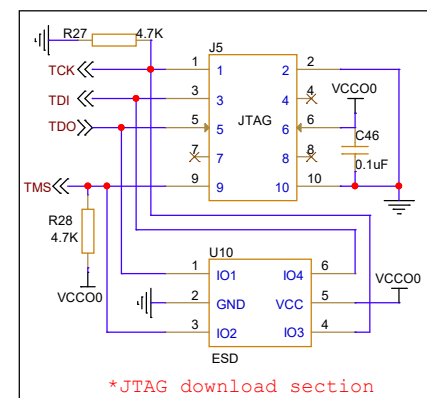
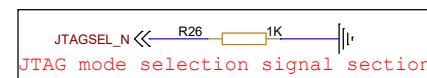
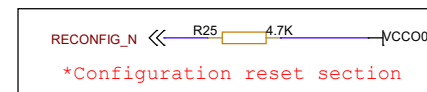
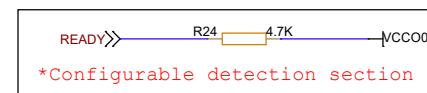
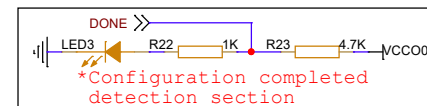
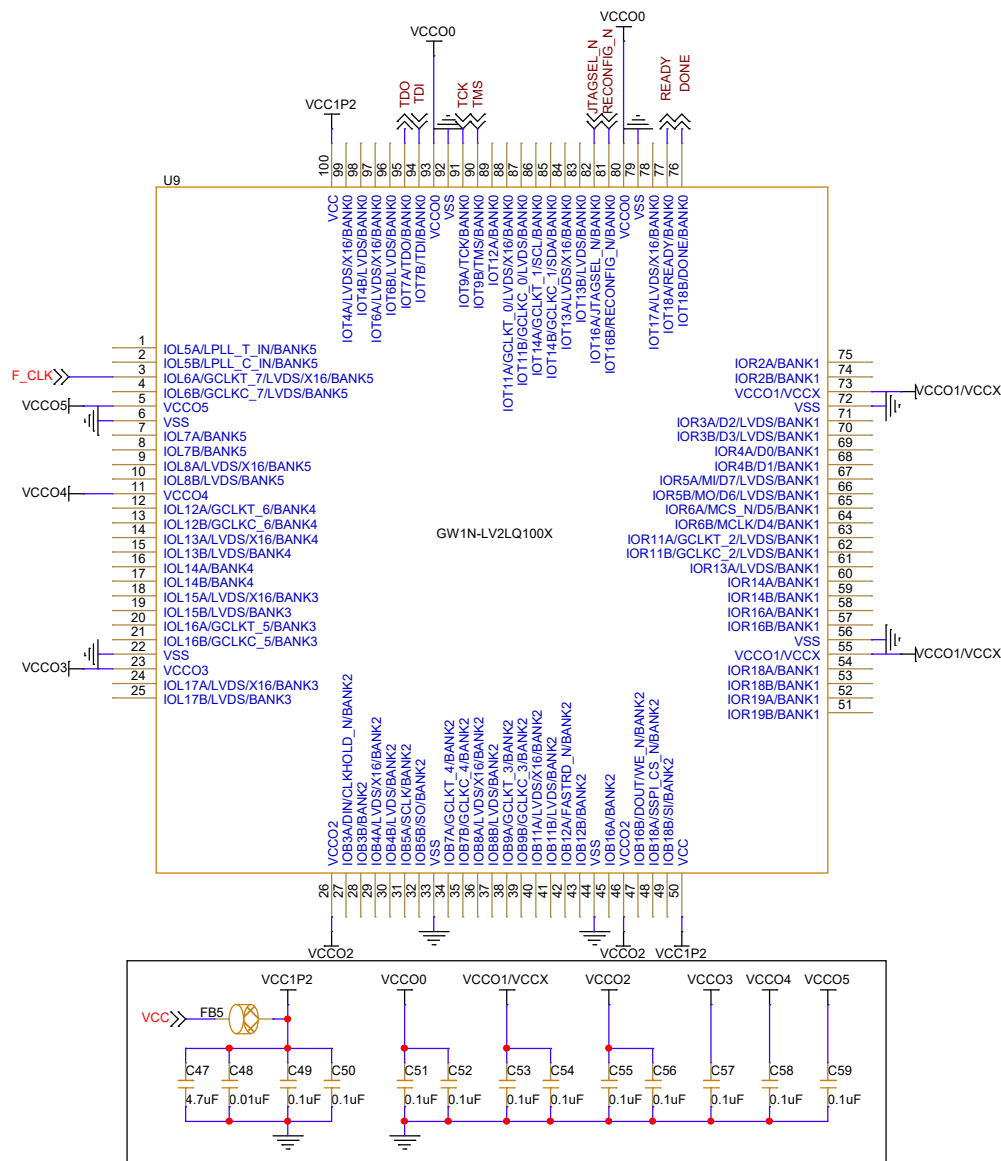


Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

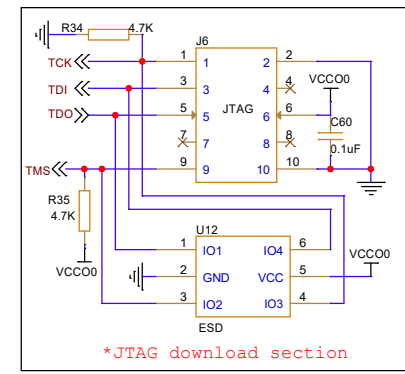
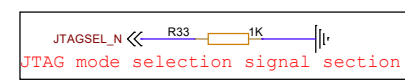
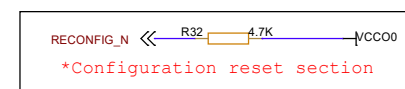
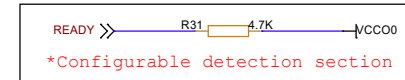
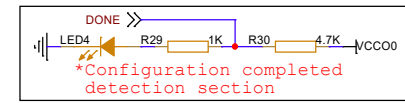
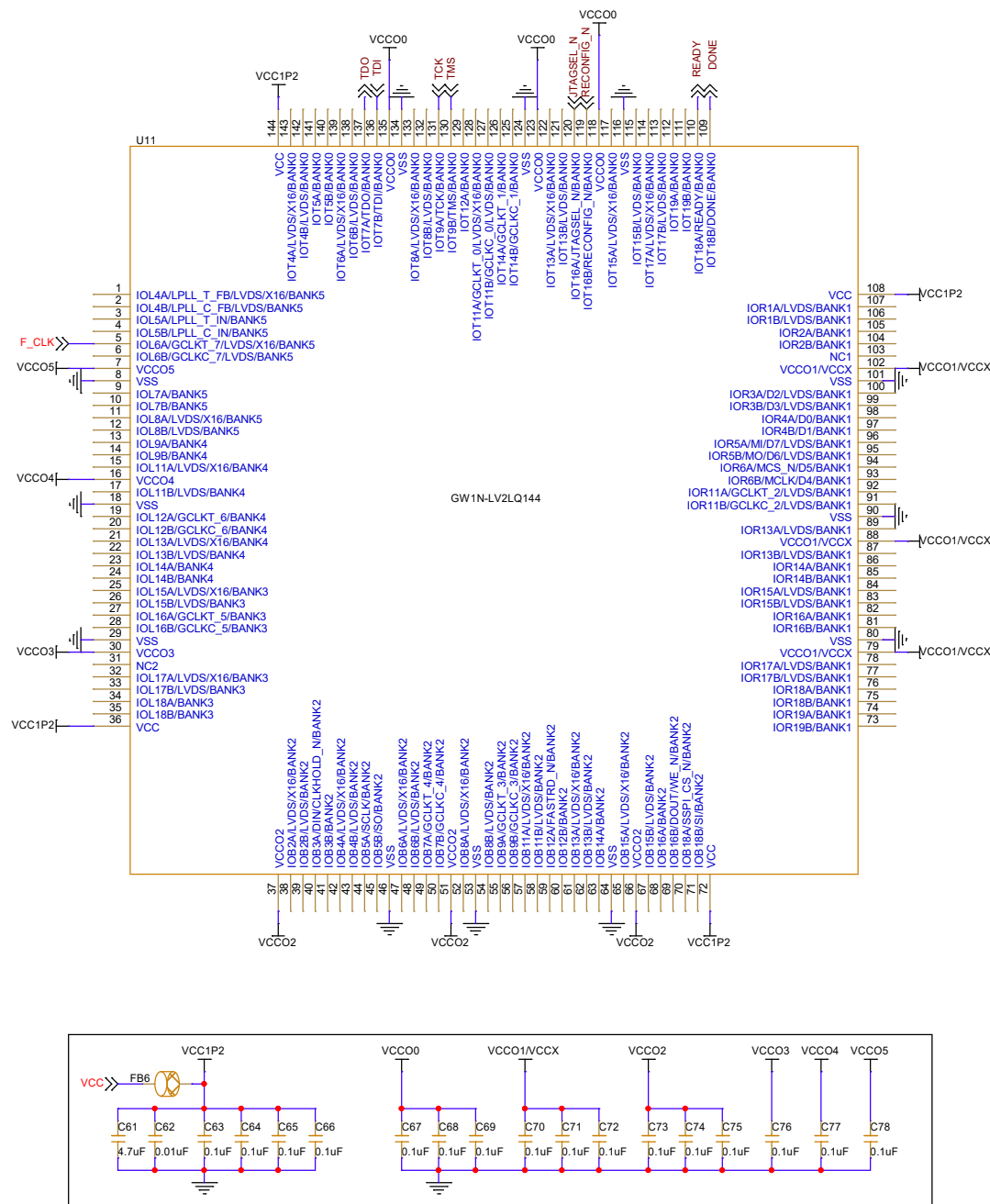


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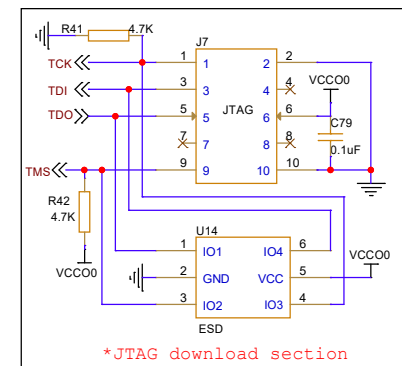


- Notes:
- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
 - 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

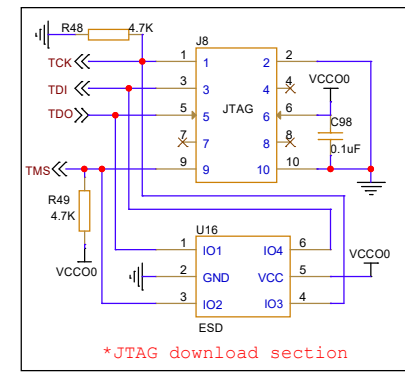
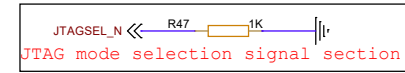
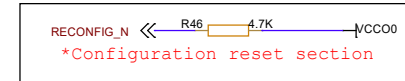
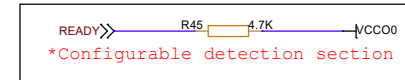
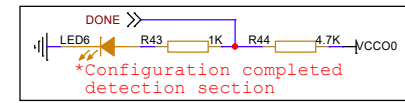
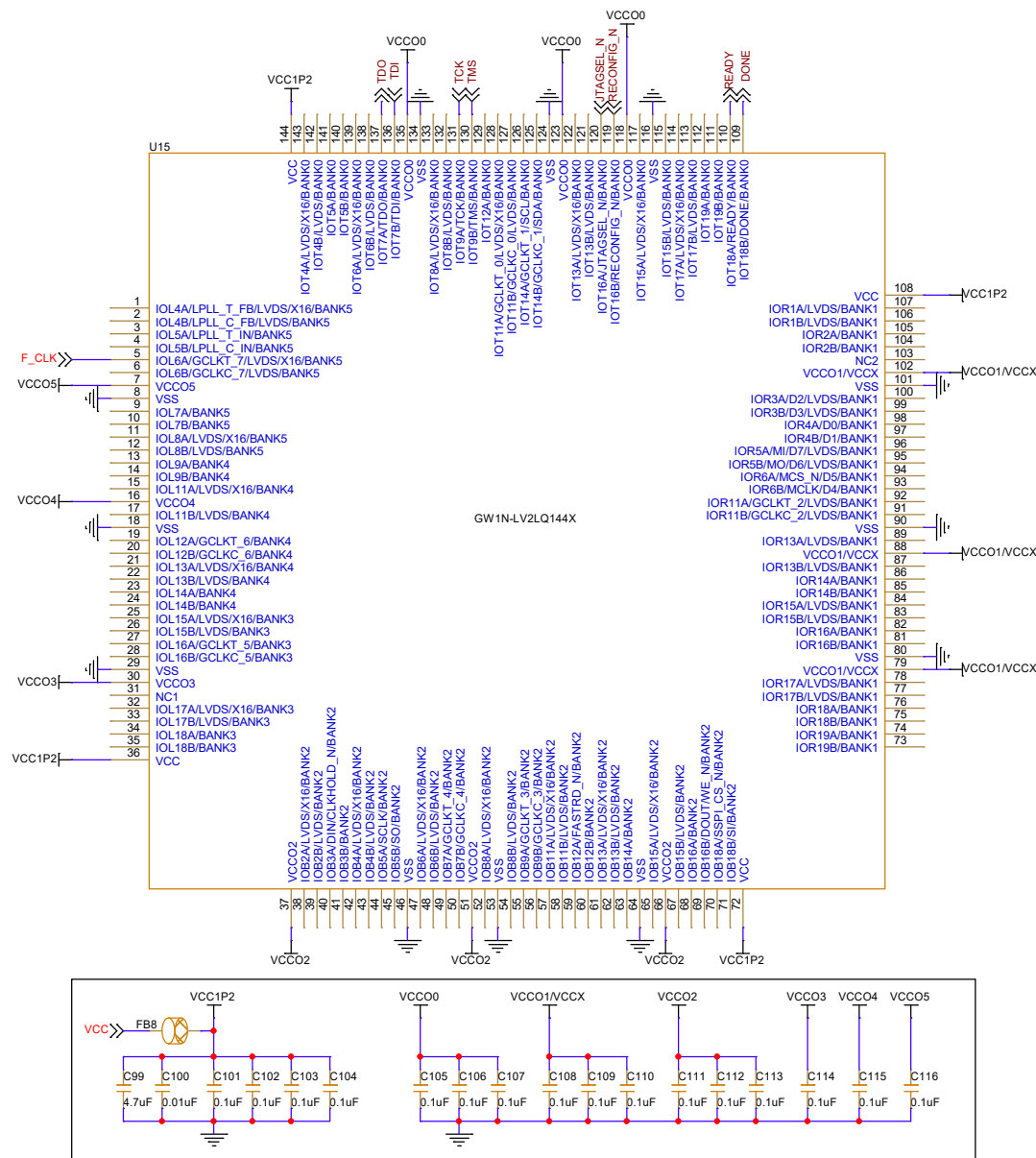
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GOWIN Minimum System SCH		
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B	GW1N-LV2LQ100X	2.3
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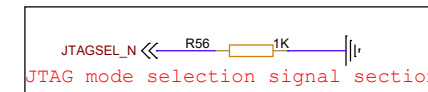
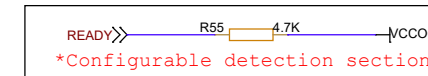
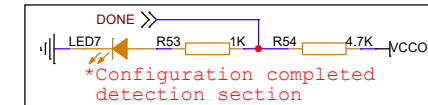
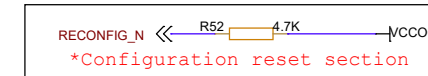
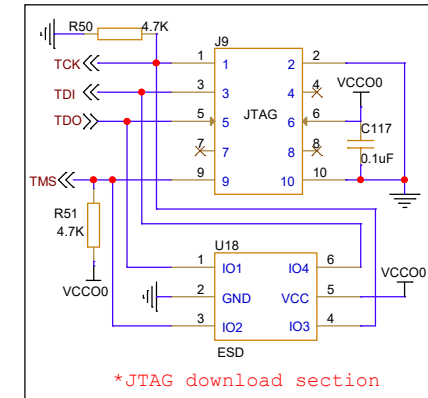
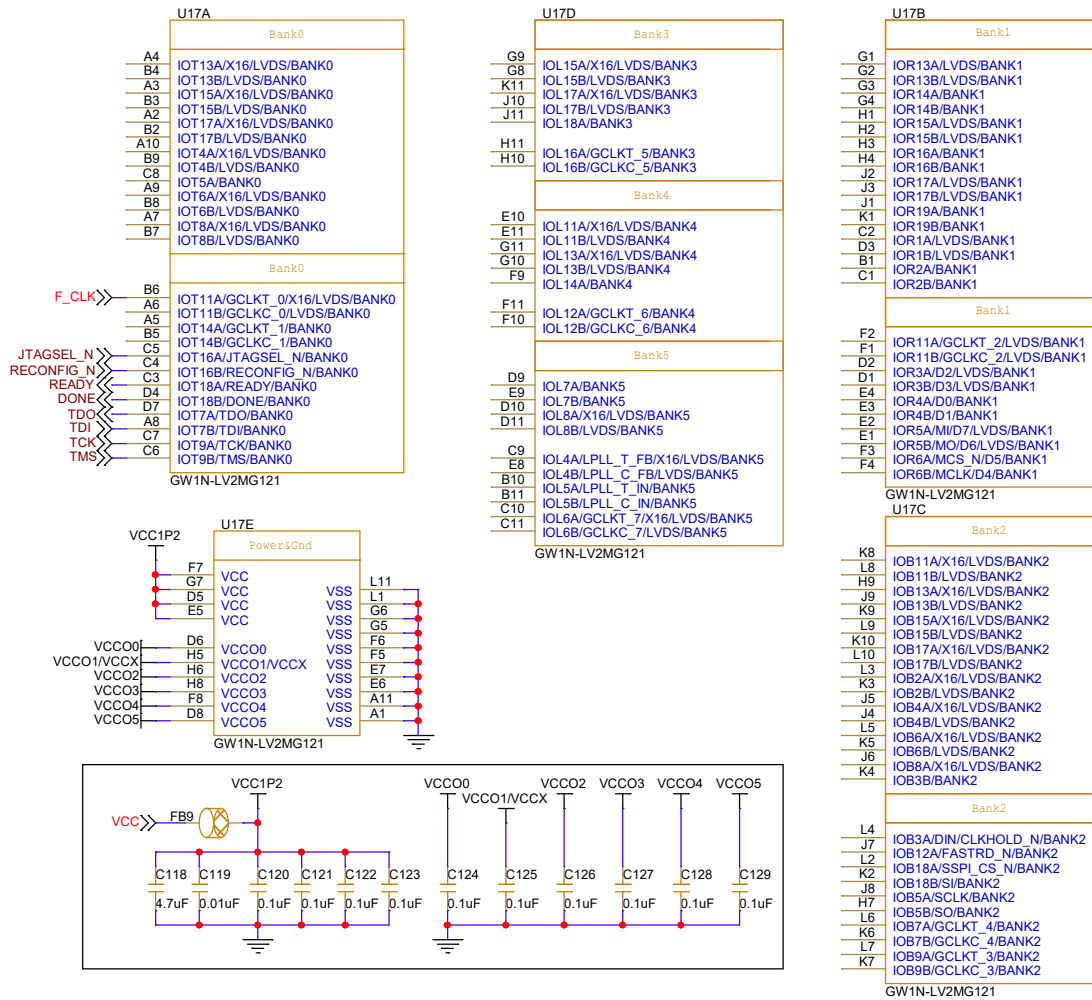
Notes:
1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.



2. It is recommended that add an ESD protection chip to the JTAG download circuit.



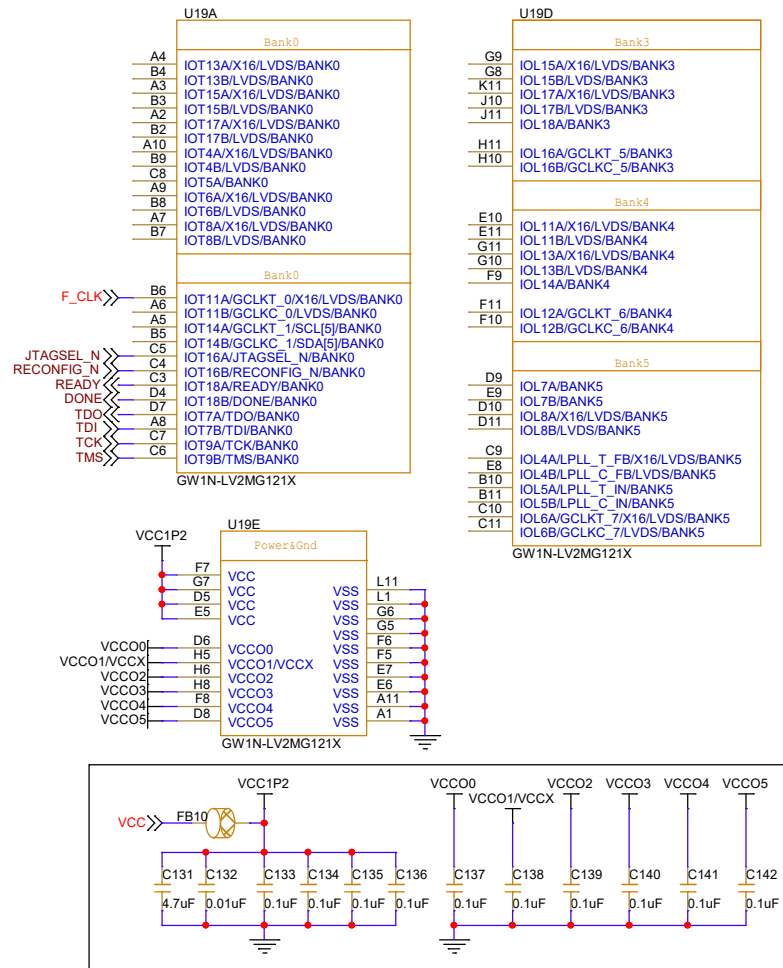
Notes:
 1.F_CLK signal is an external input clock signal.
 It is recommended that F_CLK signal be provided through an active oscillator crystal.
 2.It is recommended that add an ESD protection chip to the JTAG download circuit.



Notes:

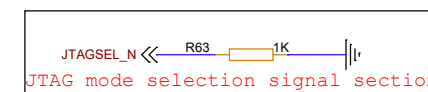
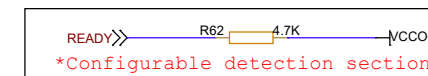
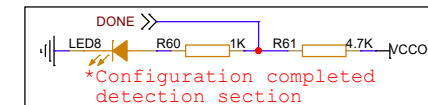
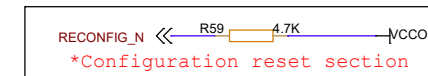
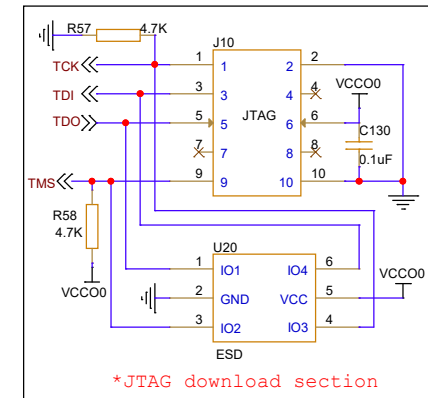
- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

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B	GW1N-LV2MG121	2.3
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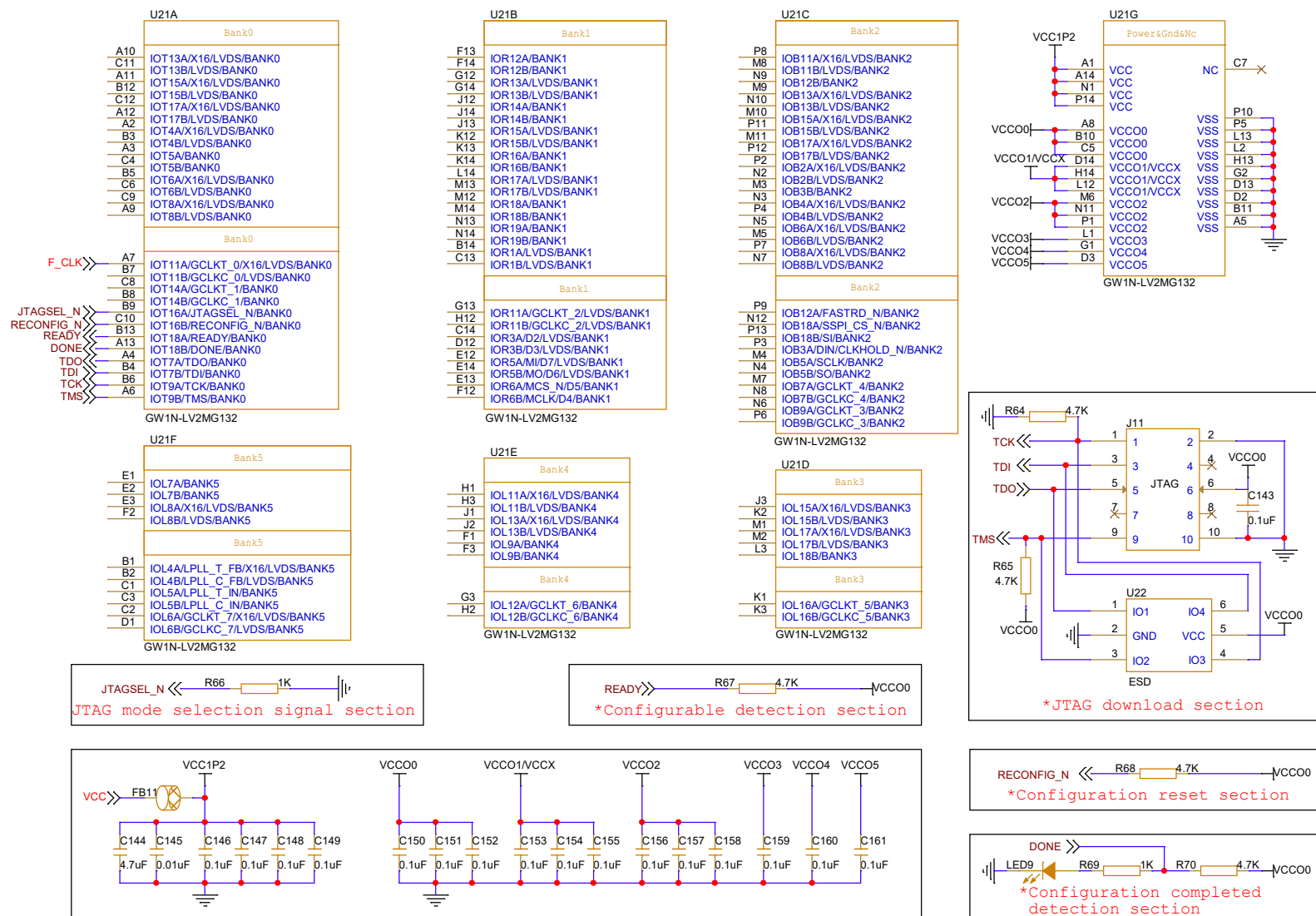


Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.



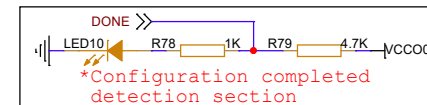
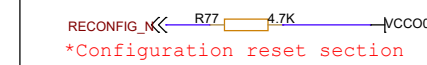
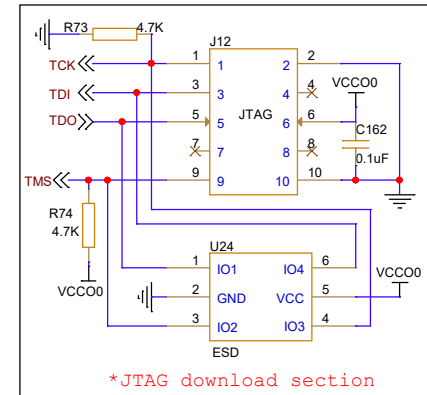
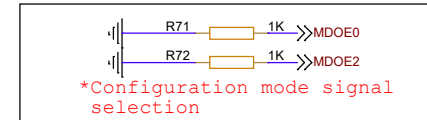
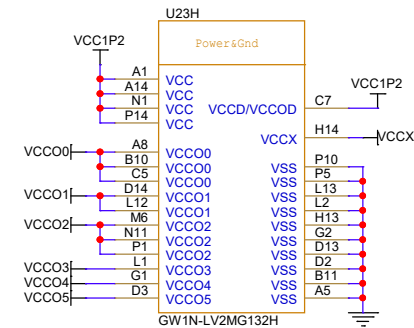
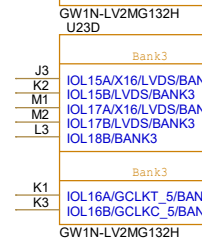
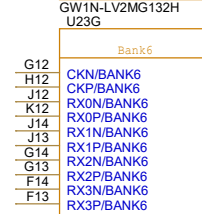
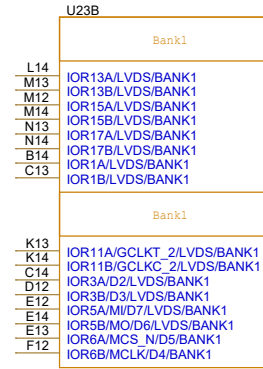
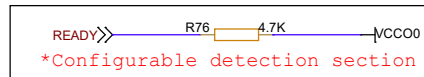
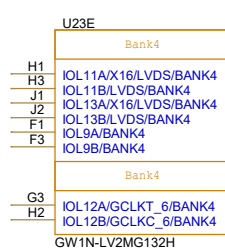
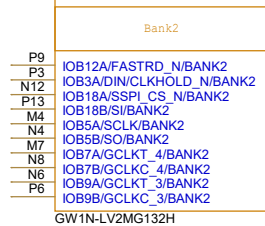
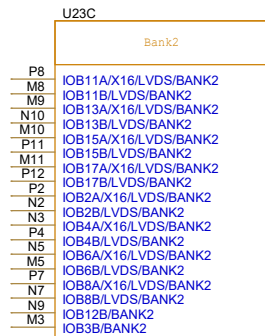
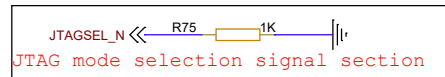
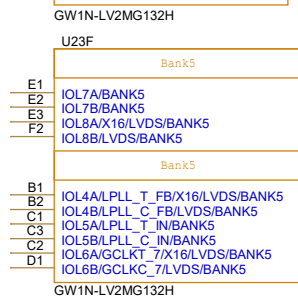
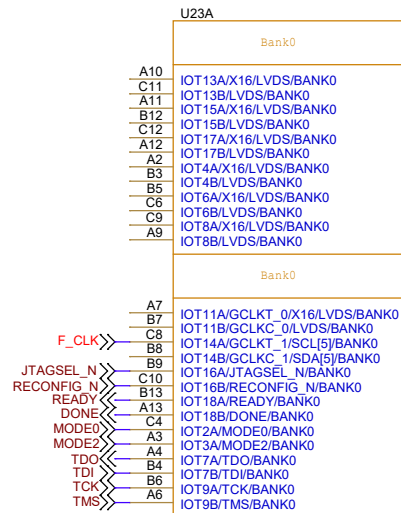
Title		
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B	GW1N-LV2MG121X	2.3
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Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

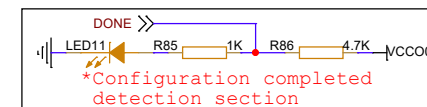
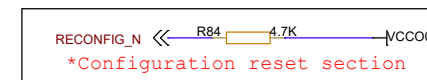
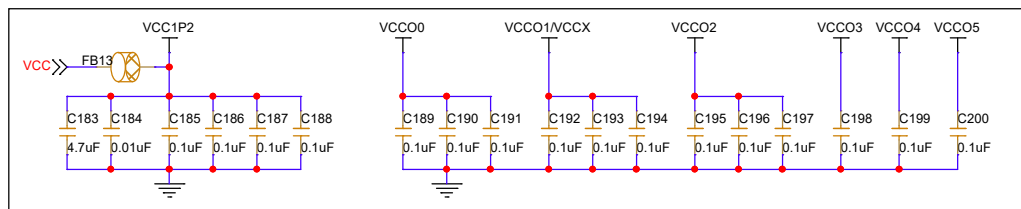
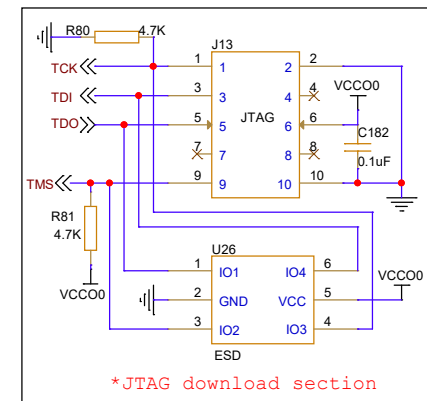
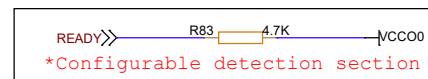
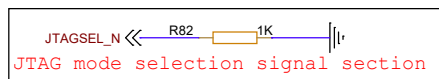
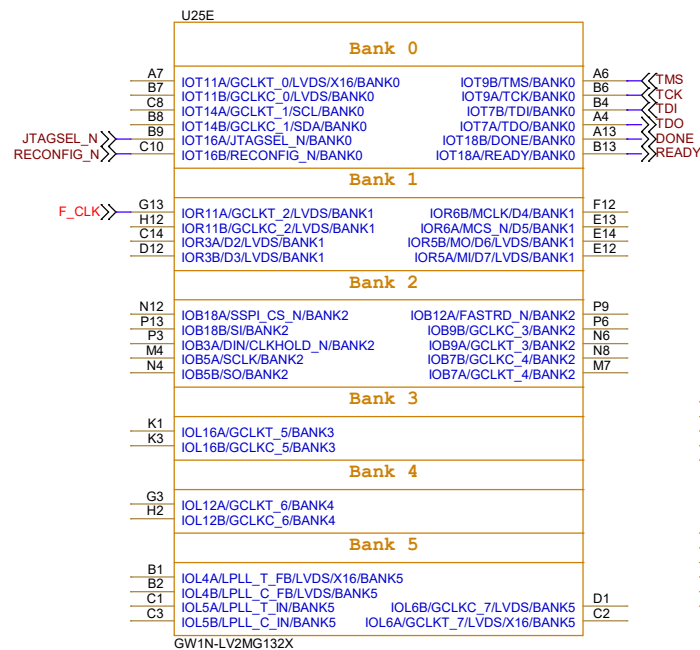
Title		
GOWIN Minimum System SCH		
Size	Document Number	Rev
B	GW1N-LV2MG132	2.3
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Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

Title				
GOWIN Minimum System SCH				
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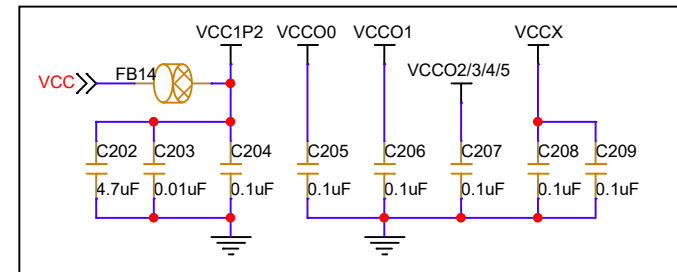
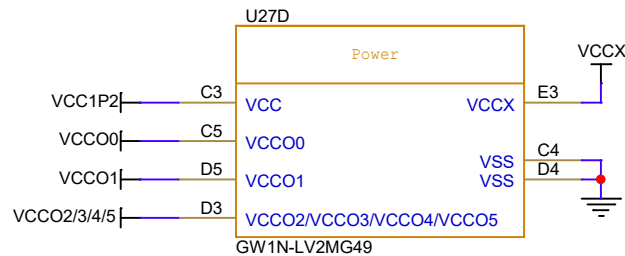
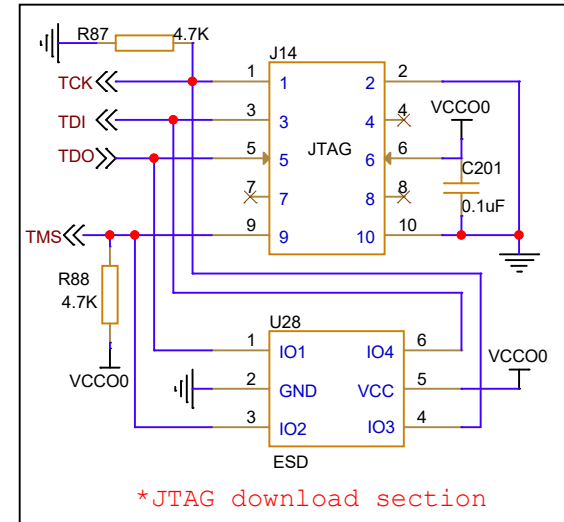
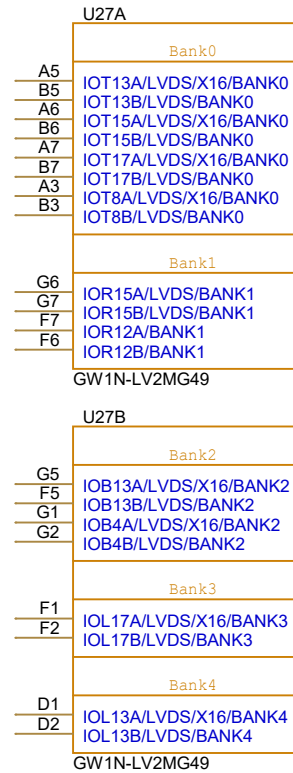
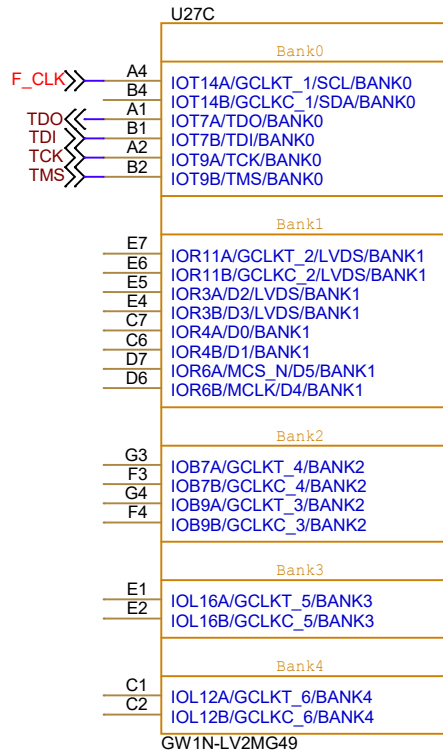


Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

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GOWIN Minimum System SCH		
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B	GW1N-LV2MG132X	2.3
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GW1N-LV2MG49



Notes:

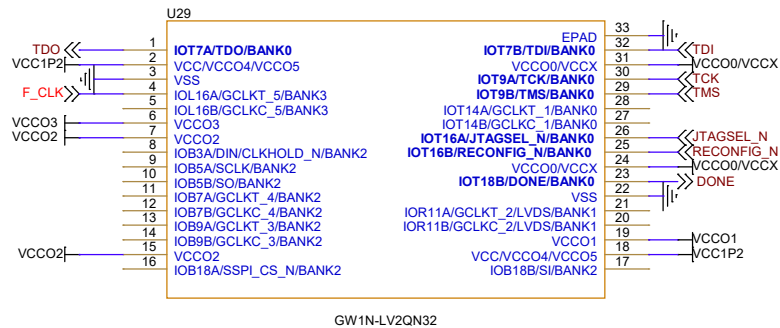
1.F_CLK signal is an external input clock signal.

It is recommended that F_CLK signal be provided through an active oscillator crystal.

2.It is recommended that add an ESD protection chip to the JTAG download circuit.

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GOWIN Minimum System SCH		
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GW1N-LV2QN32



RECONFIG_N <<< R89 4.7K VCCO0/VCCX

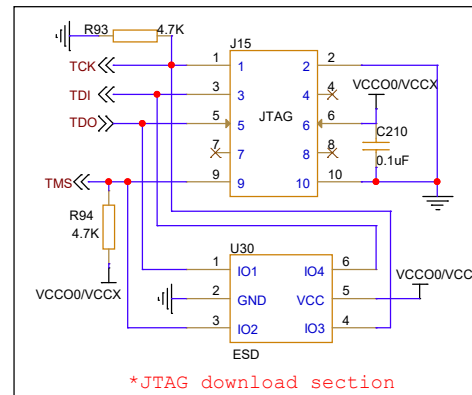
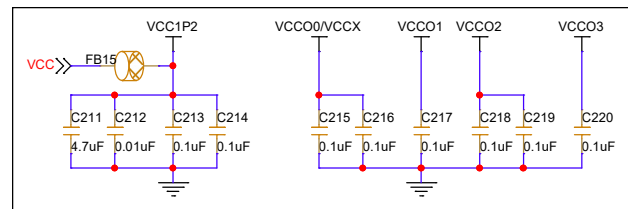
*Configuration reset section

DONE <<< LED12 R90 1K R91 4.7K VCCO0/VCCX

*Configuration completed detection section

JTAGSEL_N <<< R92 1K

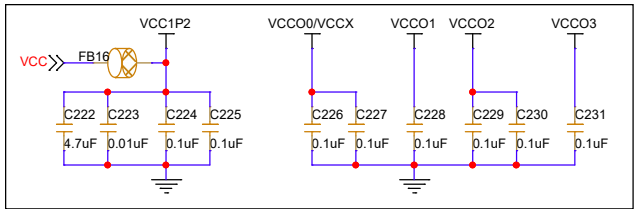
JTAG mode selection signal section



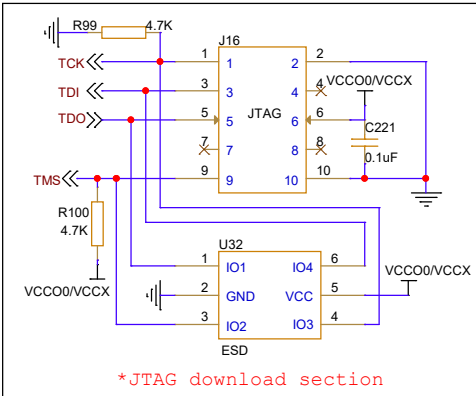
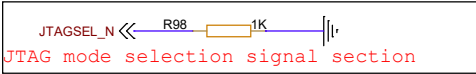
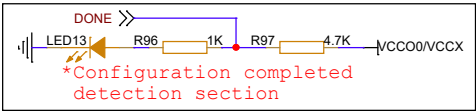
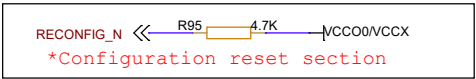
Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

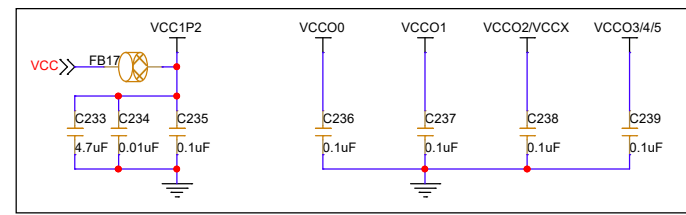
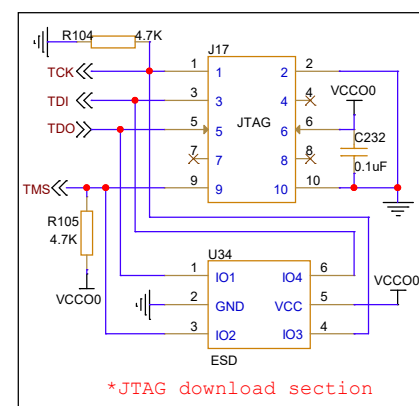
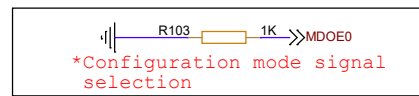
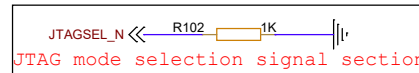
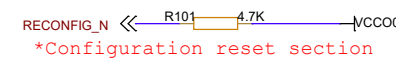
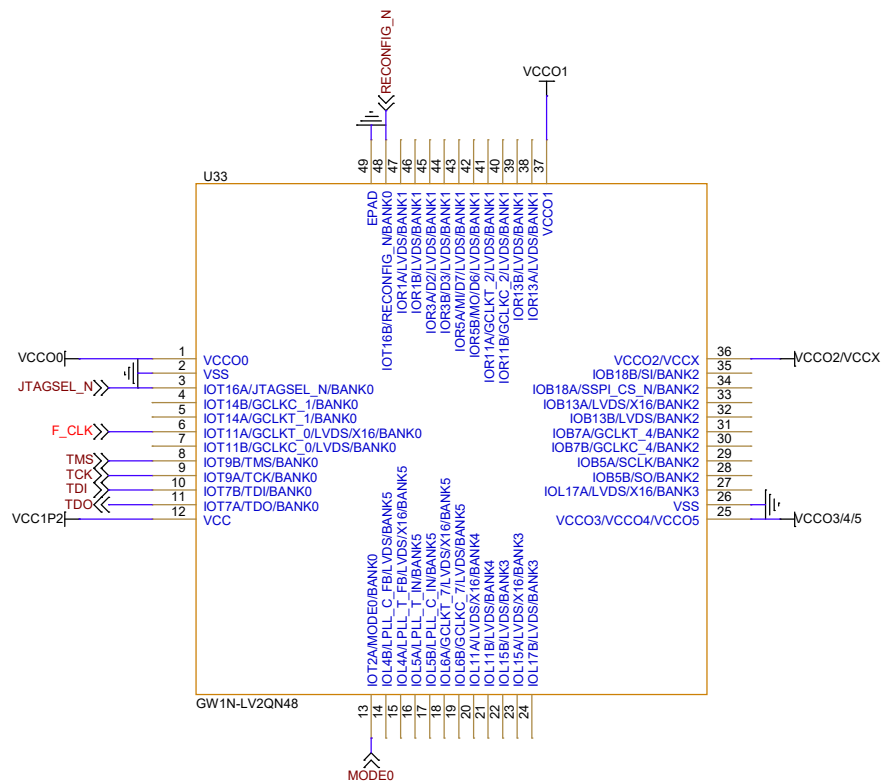
Title		
GOWIN Minimum System SCH		
Size	Document Number	Rev
B	GW1N-LV2QN32	2.4
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Notes:
1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

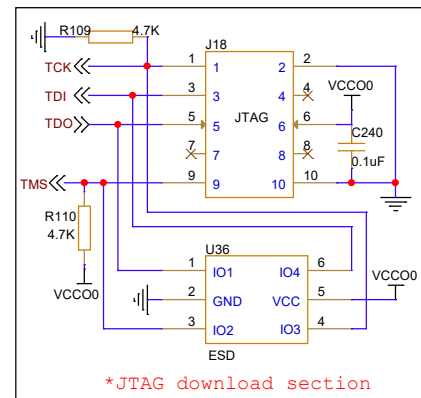
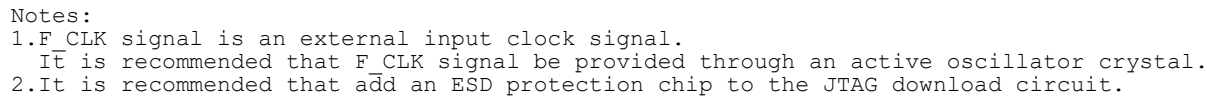


GW1N-LV2QN48



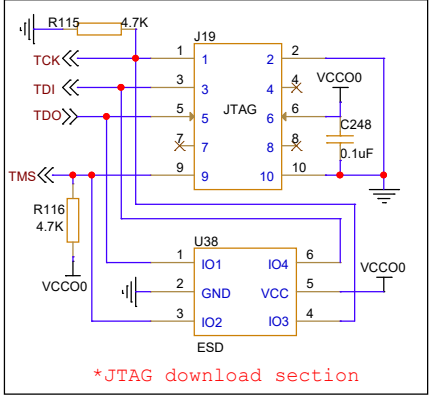
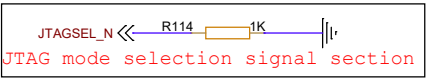
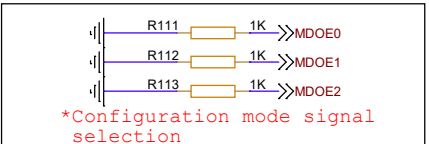
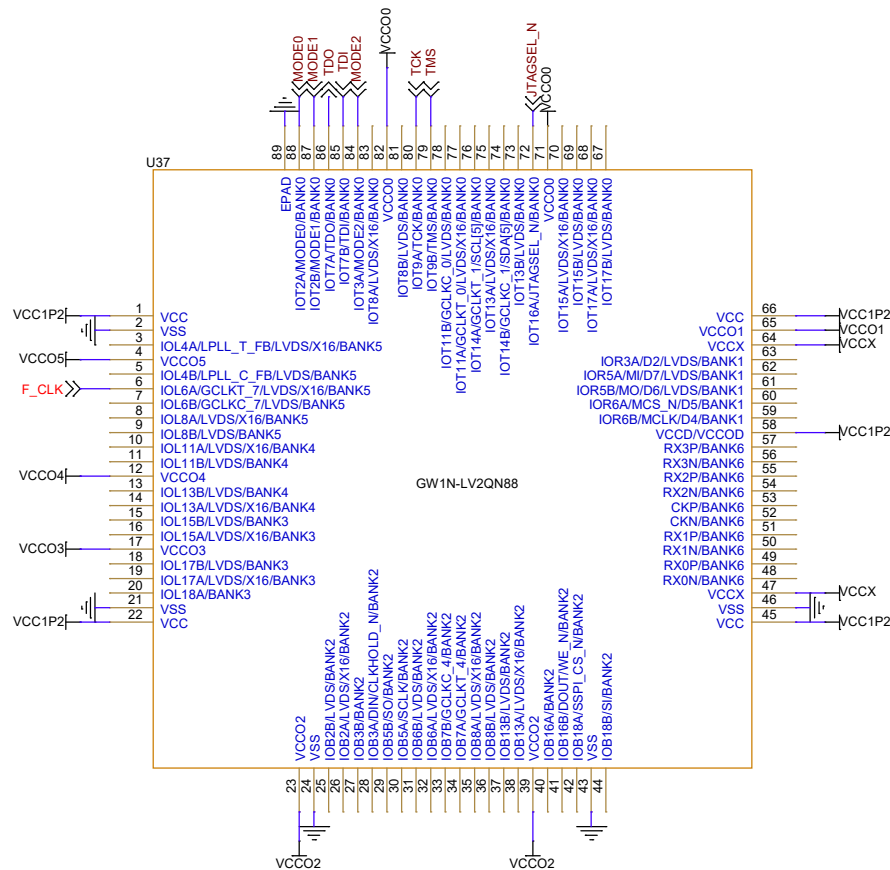
- Notes:
- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
 - 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

Title		
GOWIN Minimum System SCH		
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B	GW1N-LV2QN48	2.3
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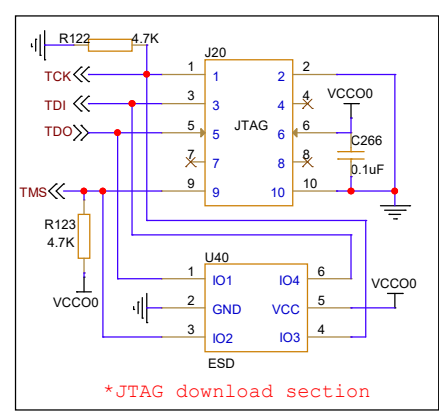
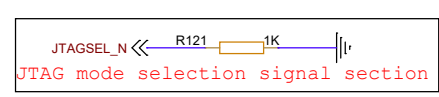
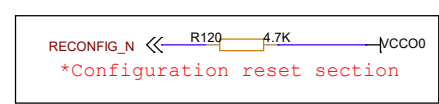
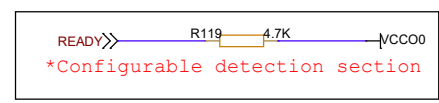
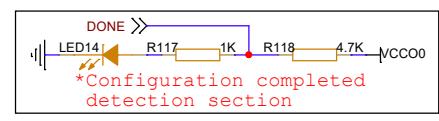
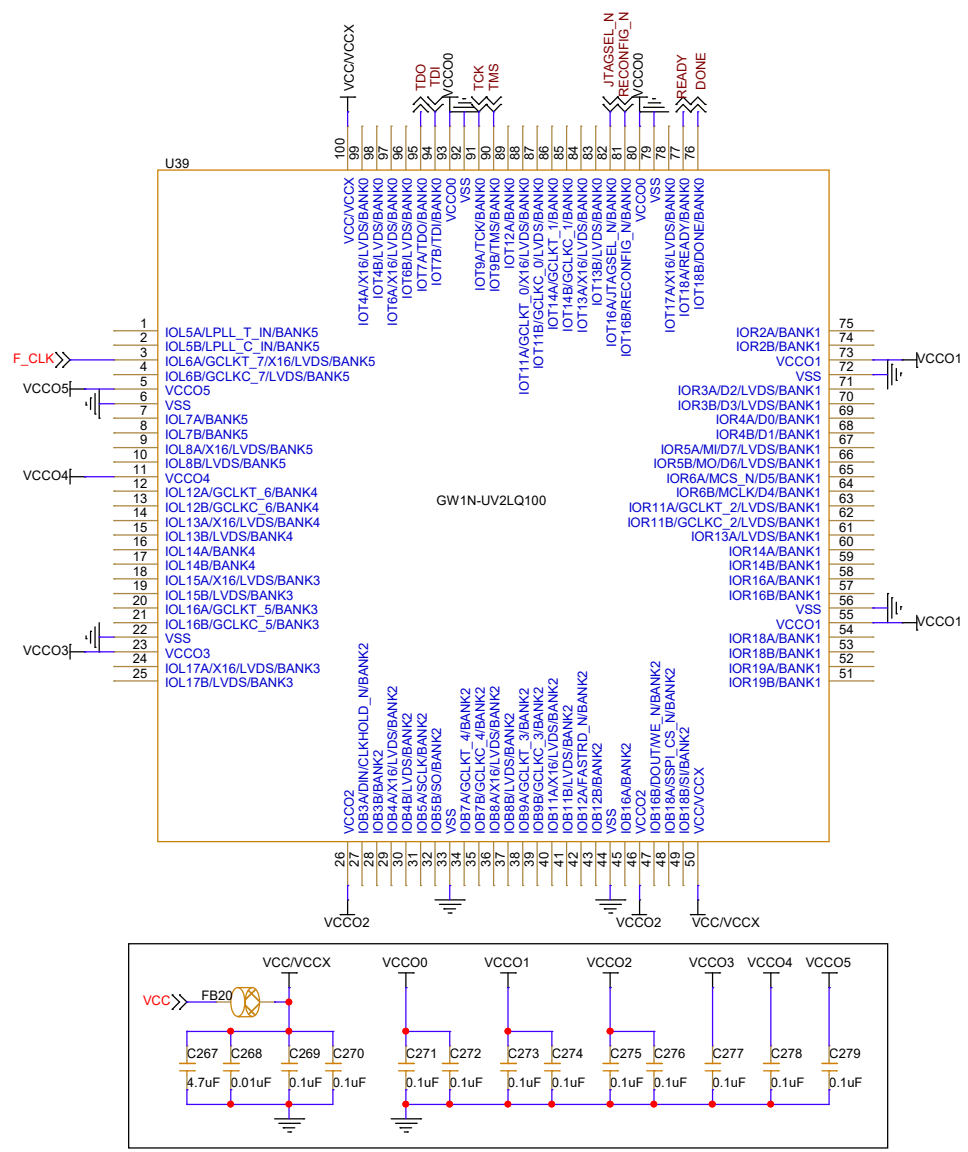
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Size B	Document Number GW1N-LV2QN48H			Rev 2.
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GW1N-LV2QN88



Notes:
 1.F CLK signal is an external input clock signal.
 It is recommended that F_CLK signal be provided through an active oscillator crystal.
 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

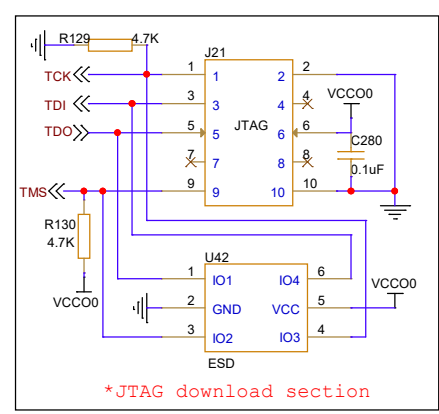
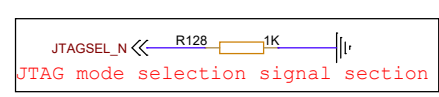
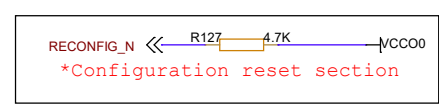
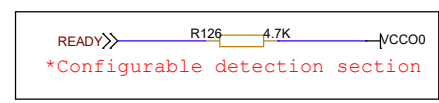
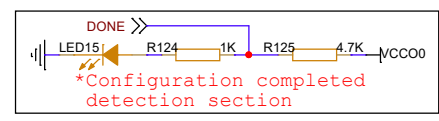
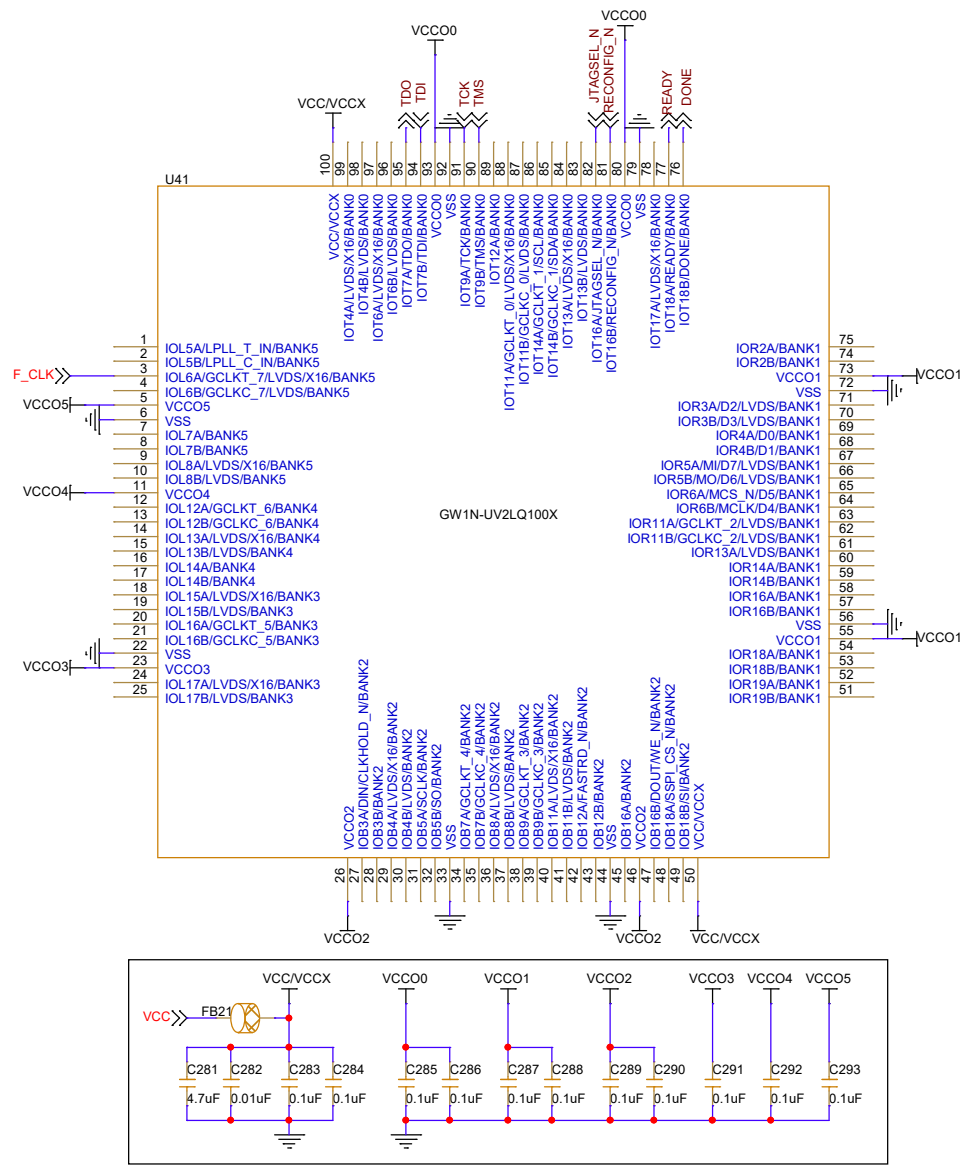
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B	GW1N-LV2QN88	2.3
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Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

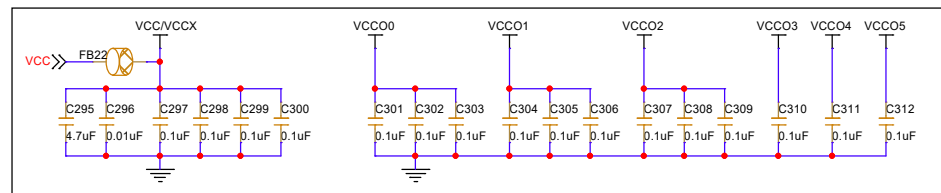
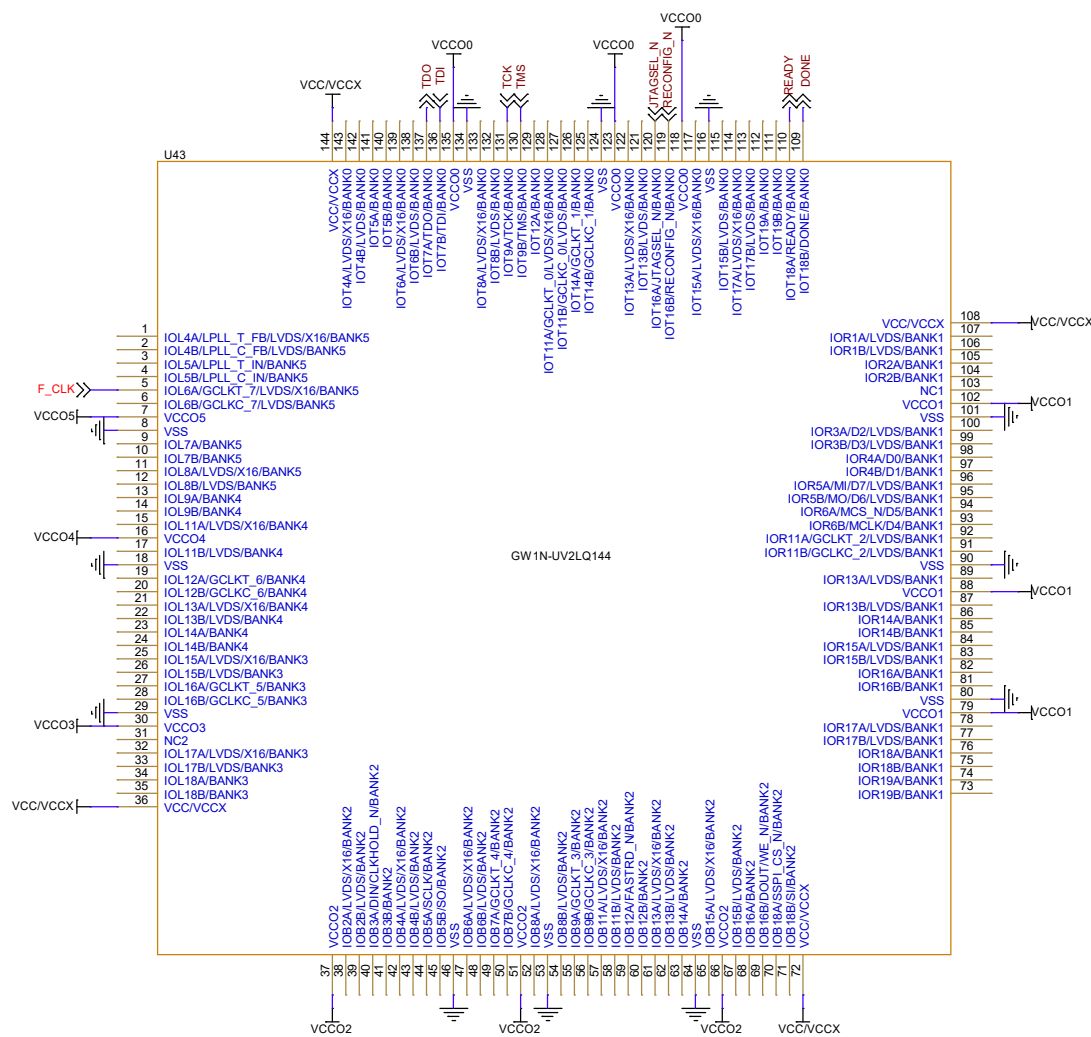
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Notes:

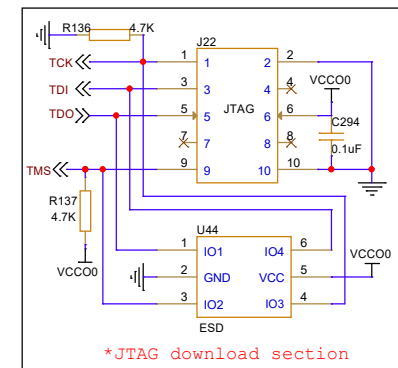
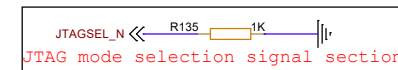
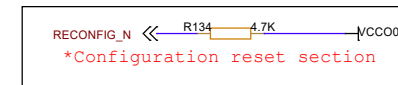
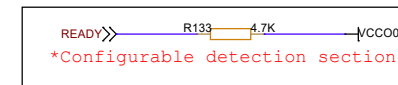
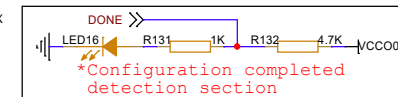
- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

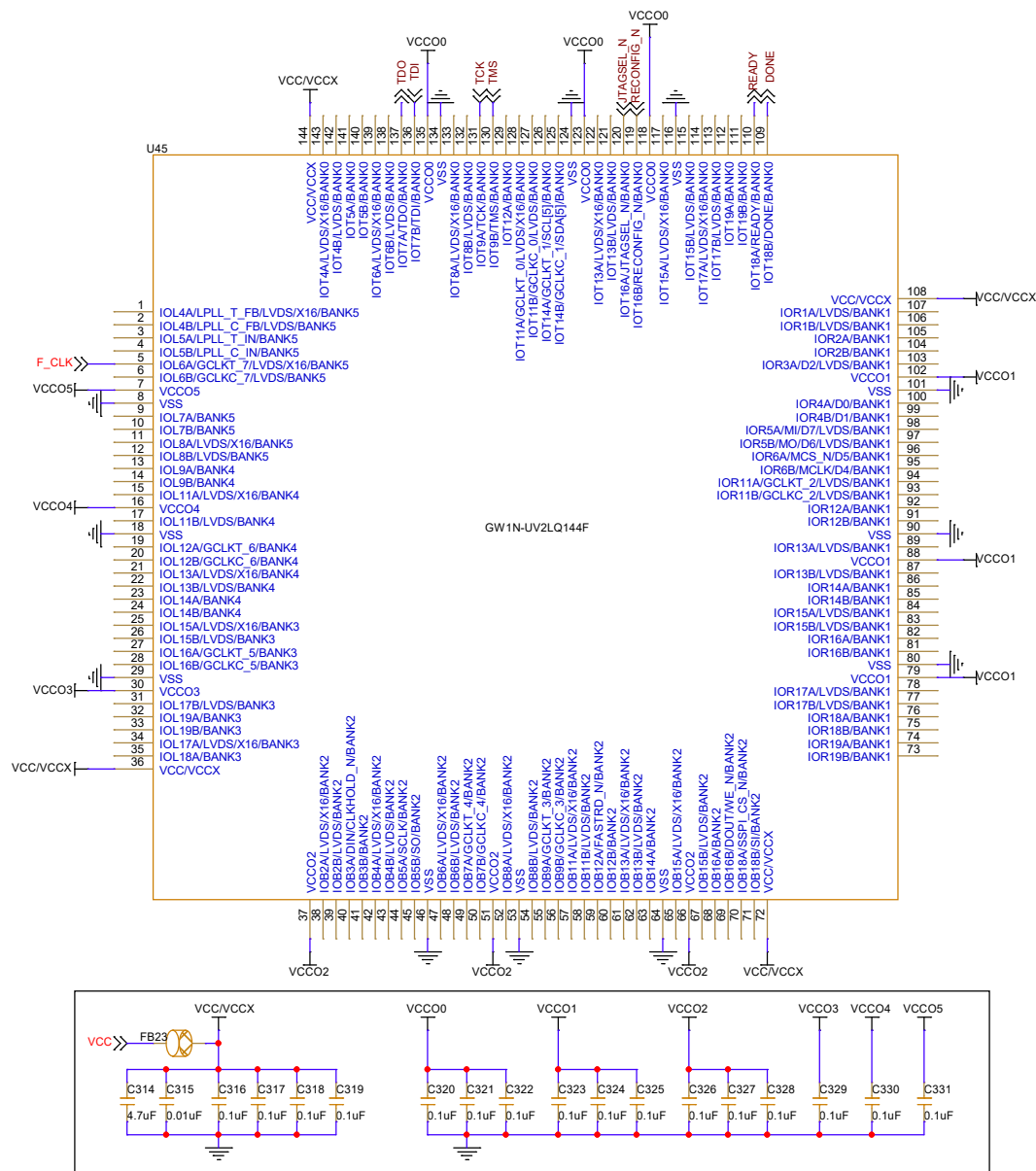
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B	GW1N-UV2LQ100X	2.3
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Notes:

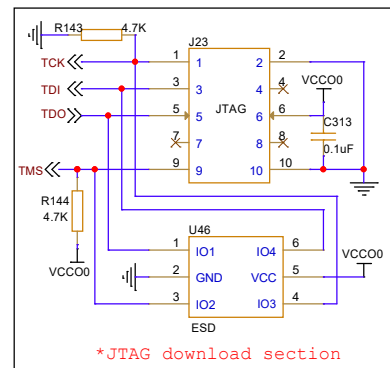
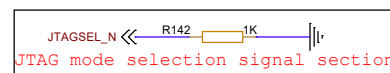
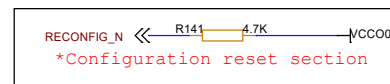
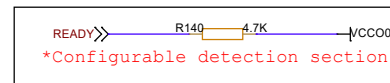
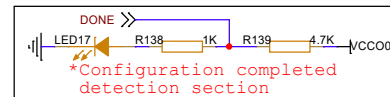
- 1.F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

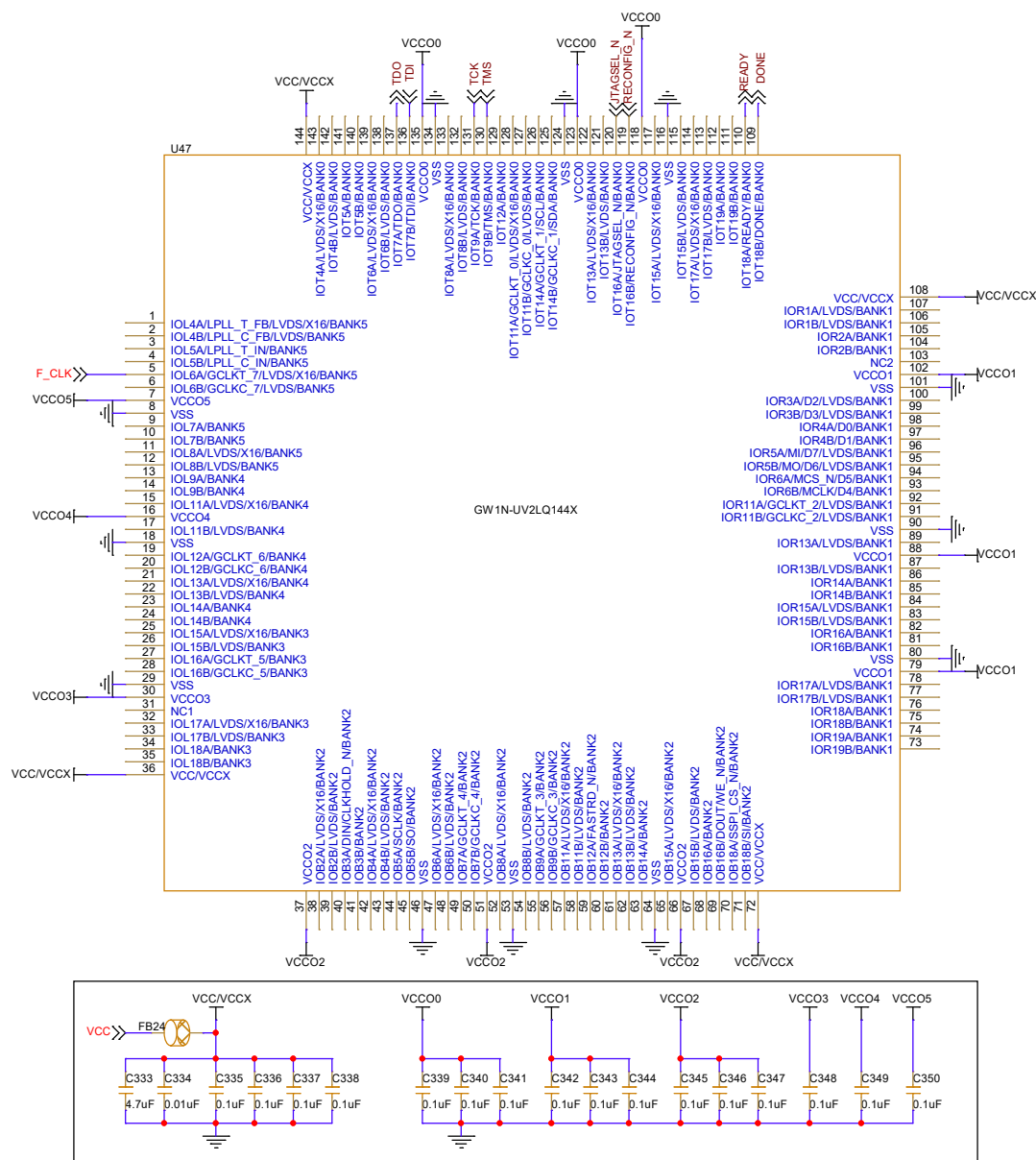




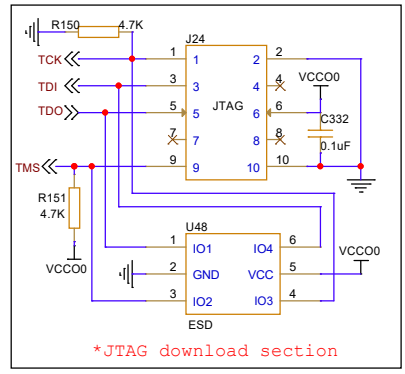
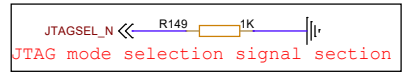
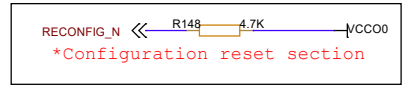
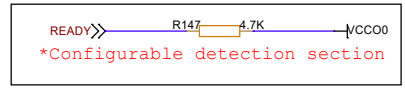
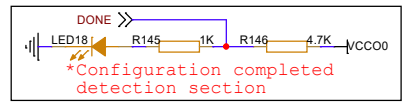
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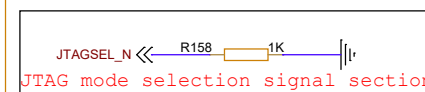
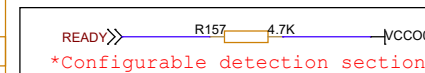
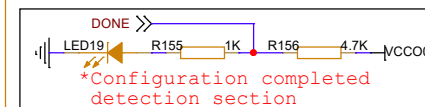
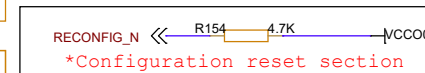
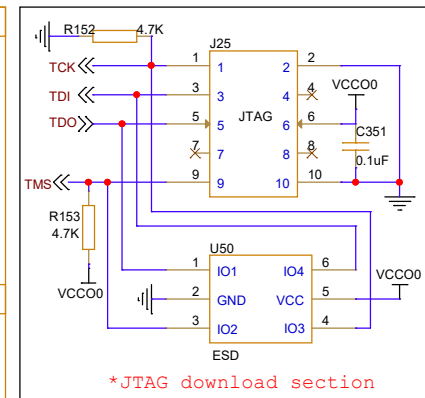
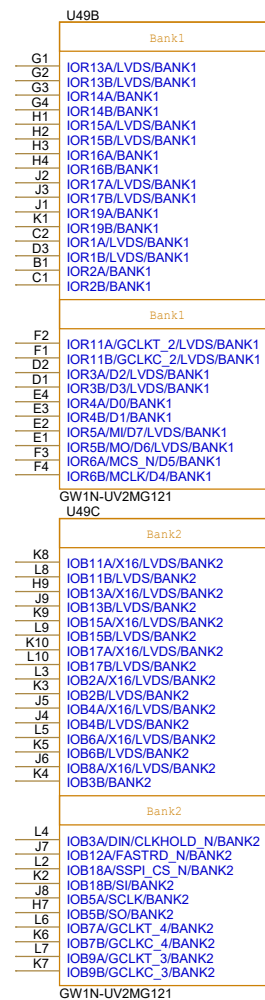
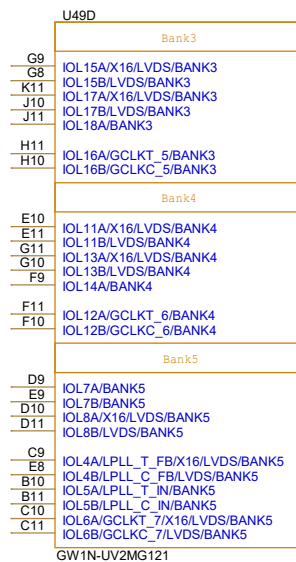
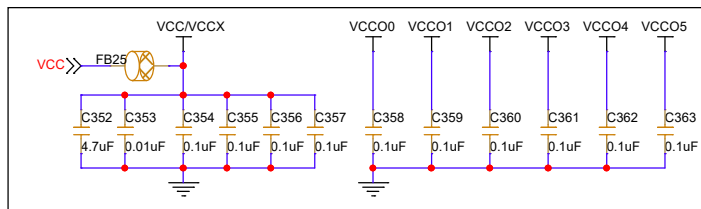
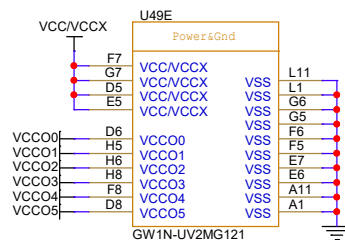
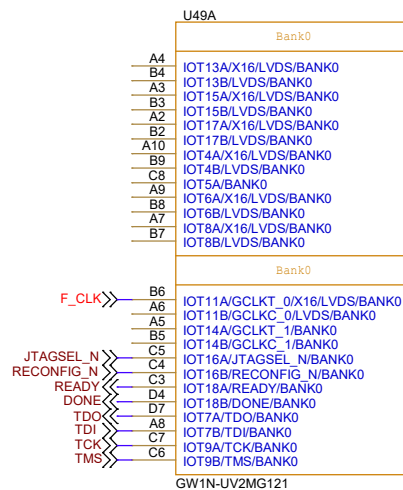
- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.





Notes:
 1.F CLK signal is an external input clock signal.
 It is recommended that F CLK signal be provided through an active oscillator crystal.
 2.It is recommended that add an ESD protection chip to the JTAG download circuit.





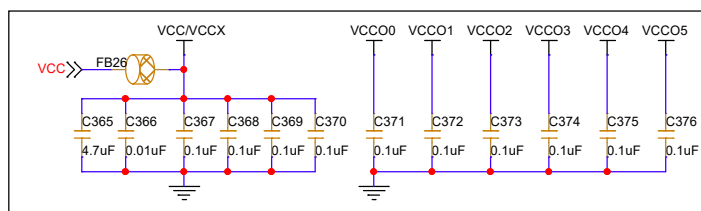
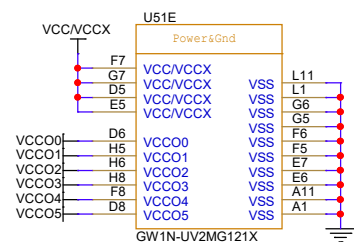
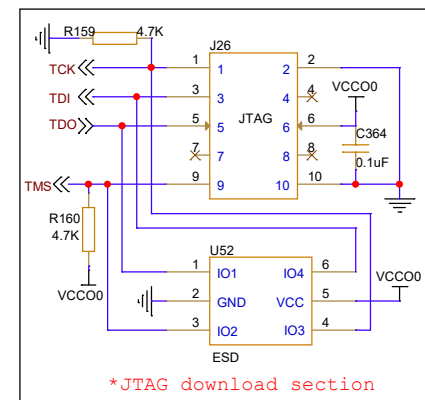
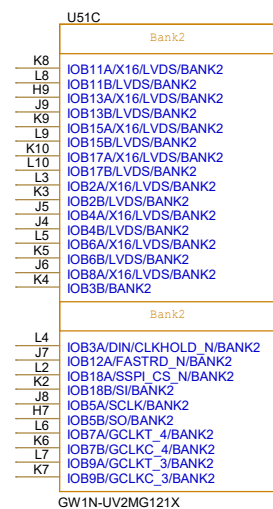
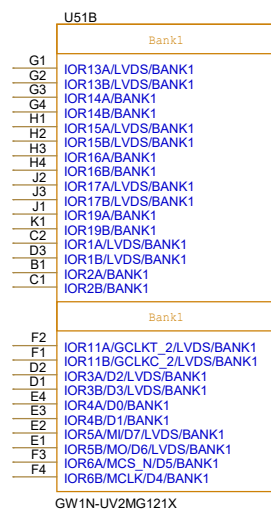
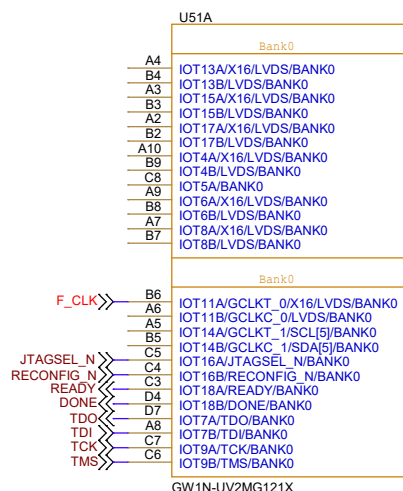
Notes:

1.F_CLK signal is an external input clock signal.

It is recommended that F_CLK signal be provided through an active oscillator crystal.

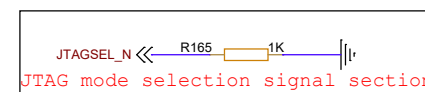
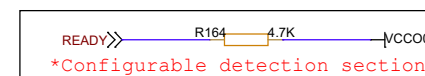
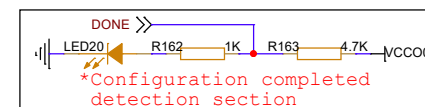
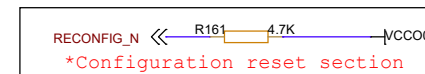
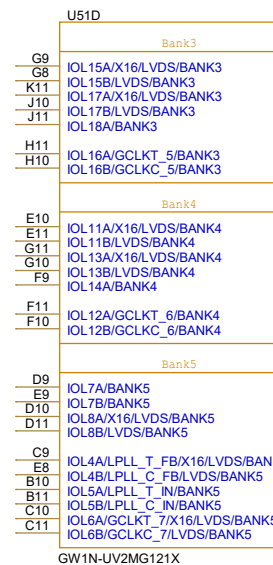
2.It is recommended that add an ESD protection chip to the JTAG download circuit.

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GOWIN Minimum System SCH		
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B	GW1N-UV2MG121	2.3
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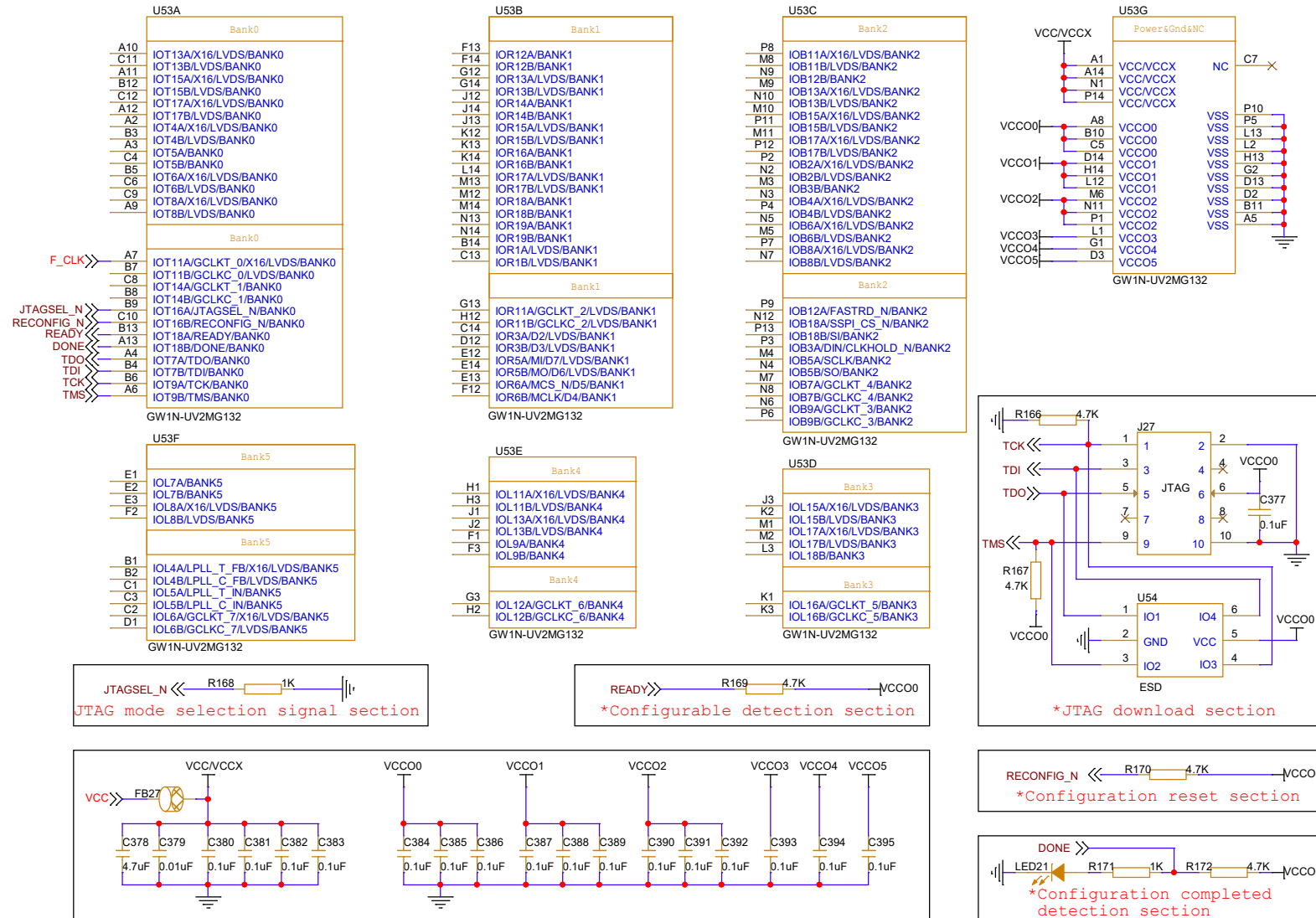


Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.



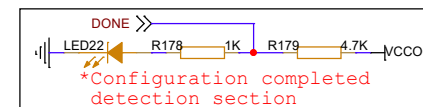
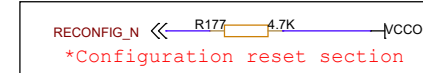
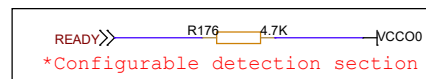
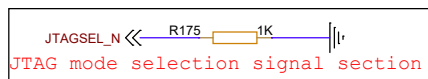
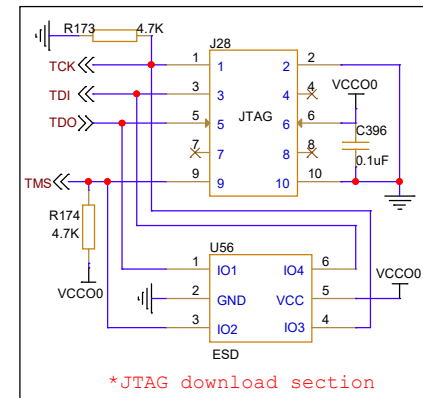
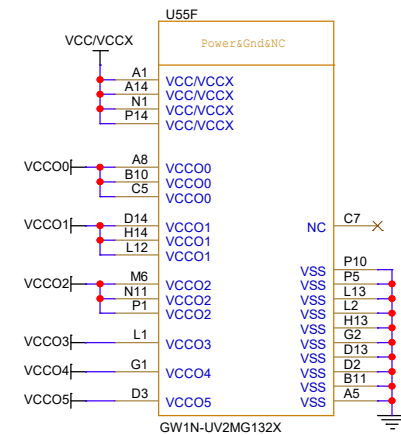
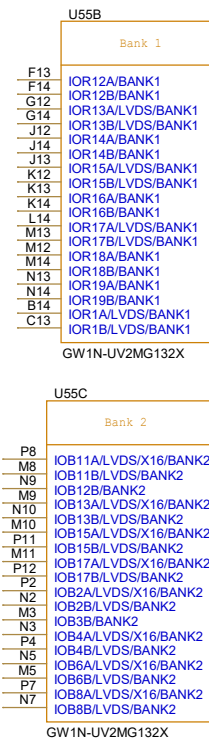
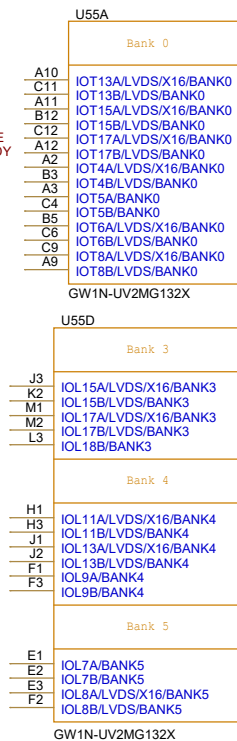
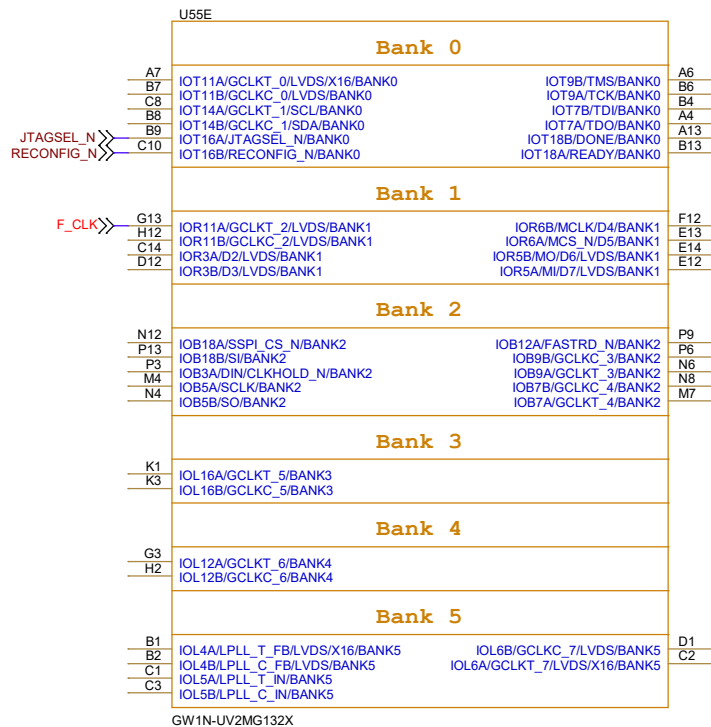
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Size B	Document Number GW1N-UV2MG121X			Rev 2.
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Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

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GOWIN Minimum System SCH		
Size	Document Number	Rev
B	GW1N-UV2MG132	2.3
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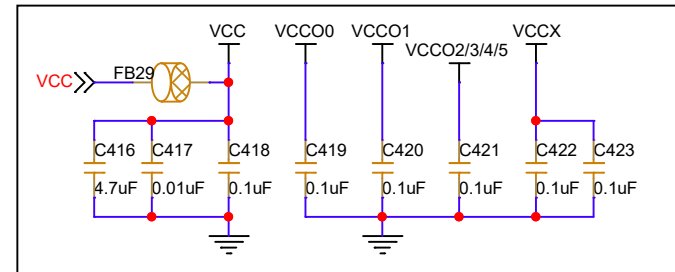
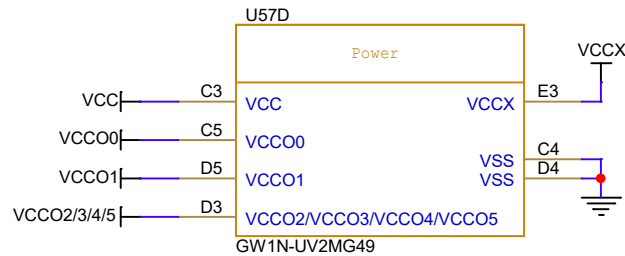
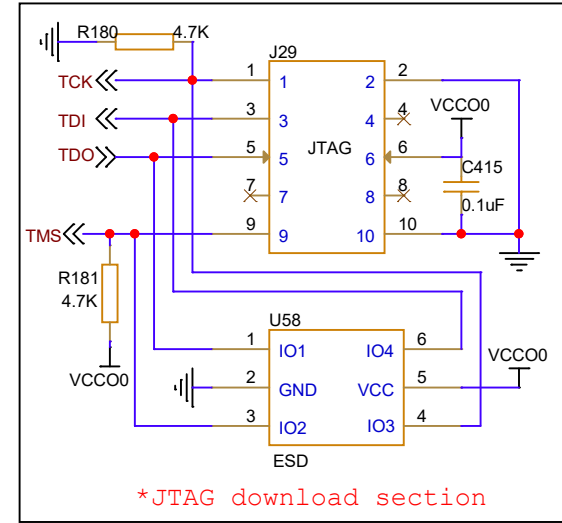
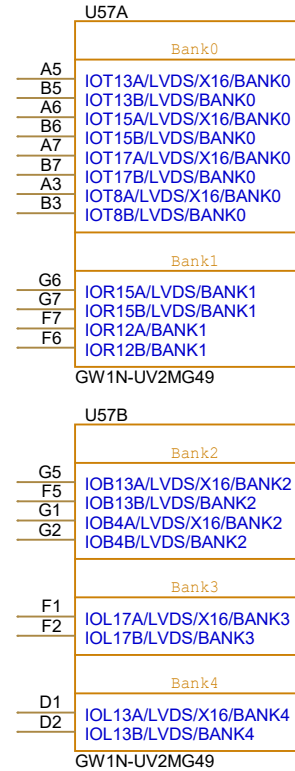
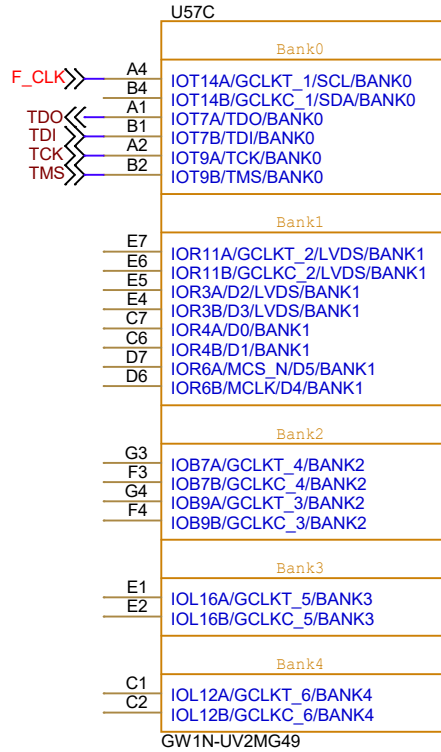


Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

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GOWIN Minimum System Diagram		
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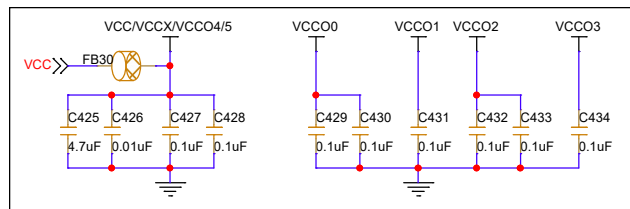
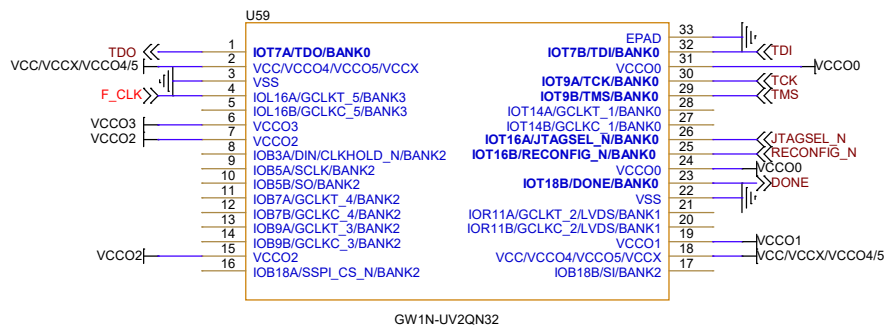
GW1N-UV2MG49



Notes:

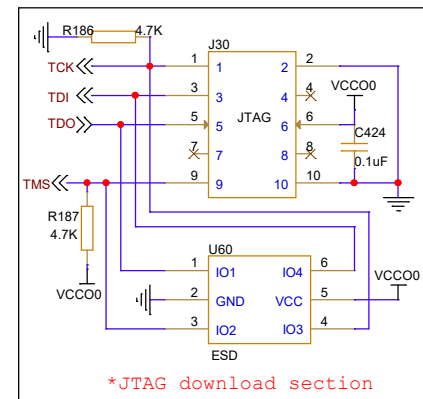
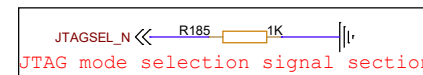
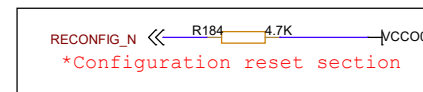
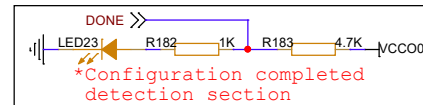
- 1.F_CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

Title		
GOWIN Minimum System SCH		
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A4	GW1N-UV2MG49	2.3
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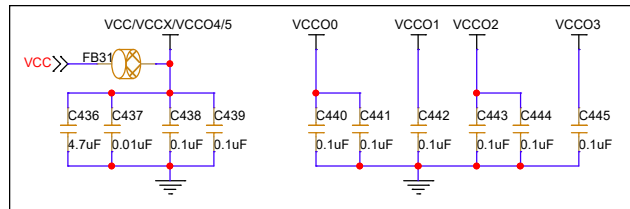
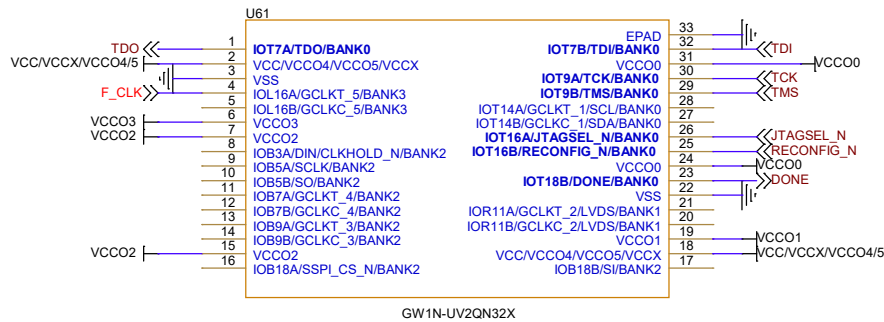


Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

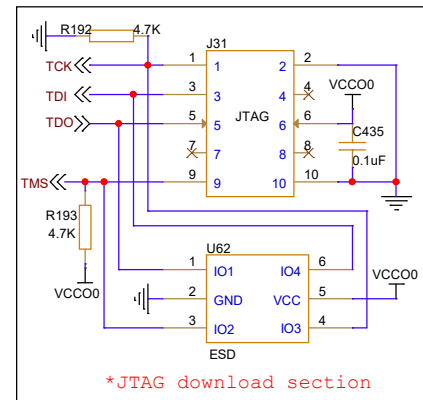
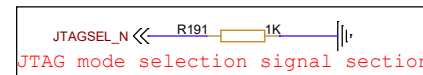
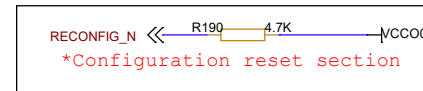
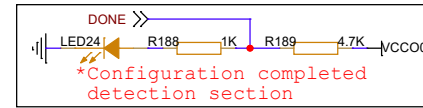


Title			
GOWIN Minimum System SCH			
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B	GW1N-UV2QN32	2.4	
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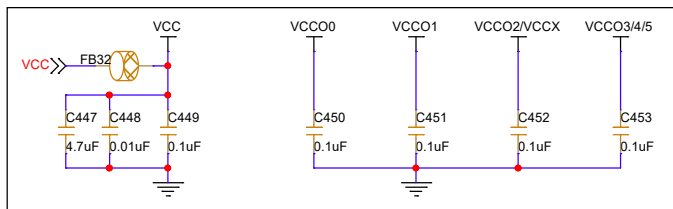
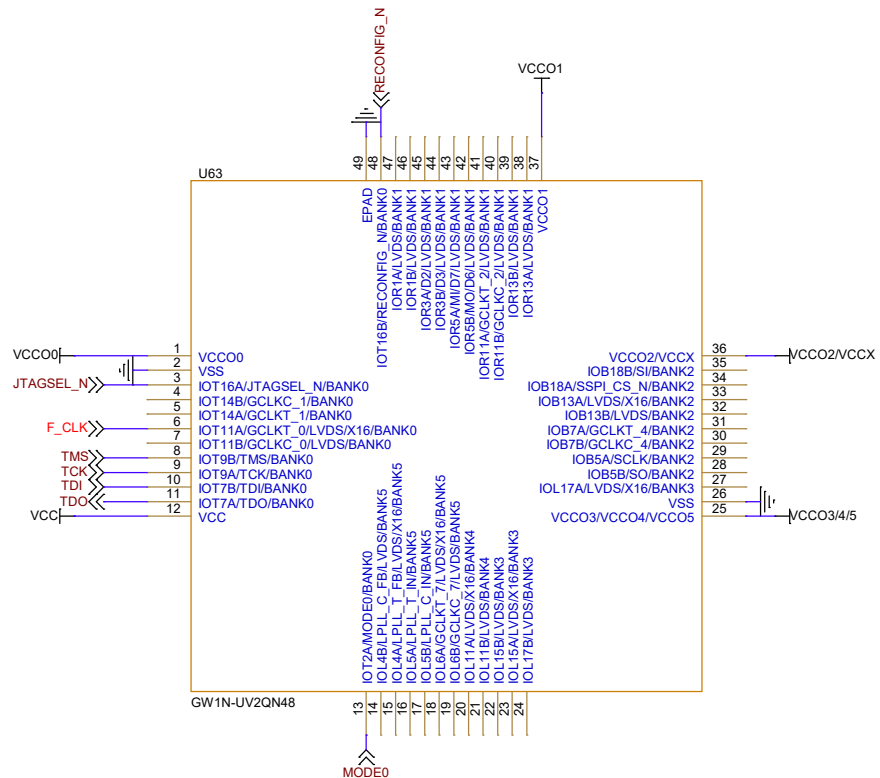
Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.



Title			
GOWIN Minimum System SCH			
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B	GW1N-UV2QN32X	2.4	
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GW1N-UV2QN48



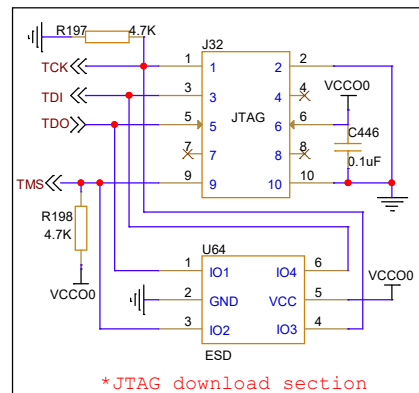
Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F CLK signal be provided through an active oscillator crystal.
- 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

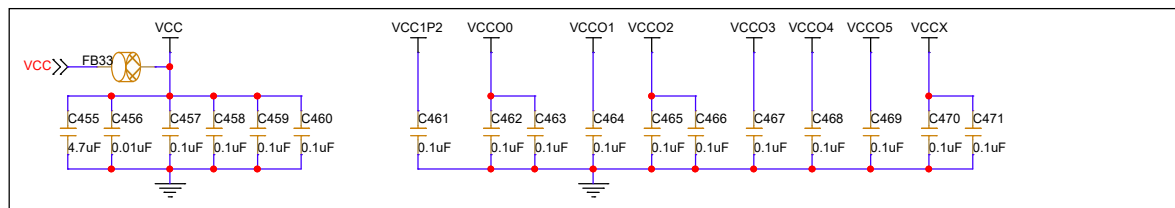
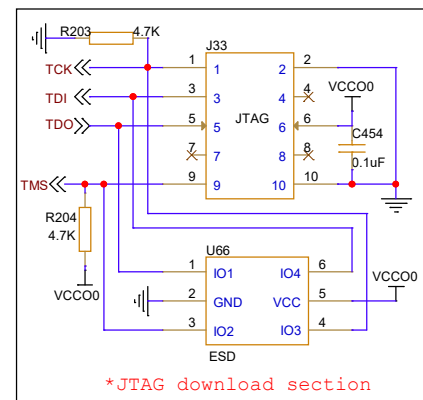
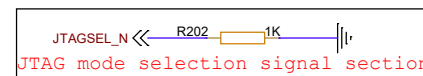
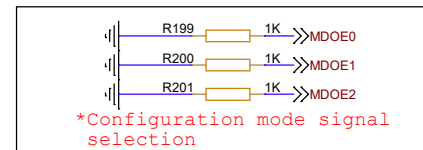
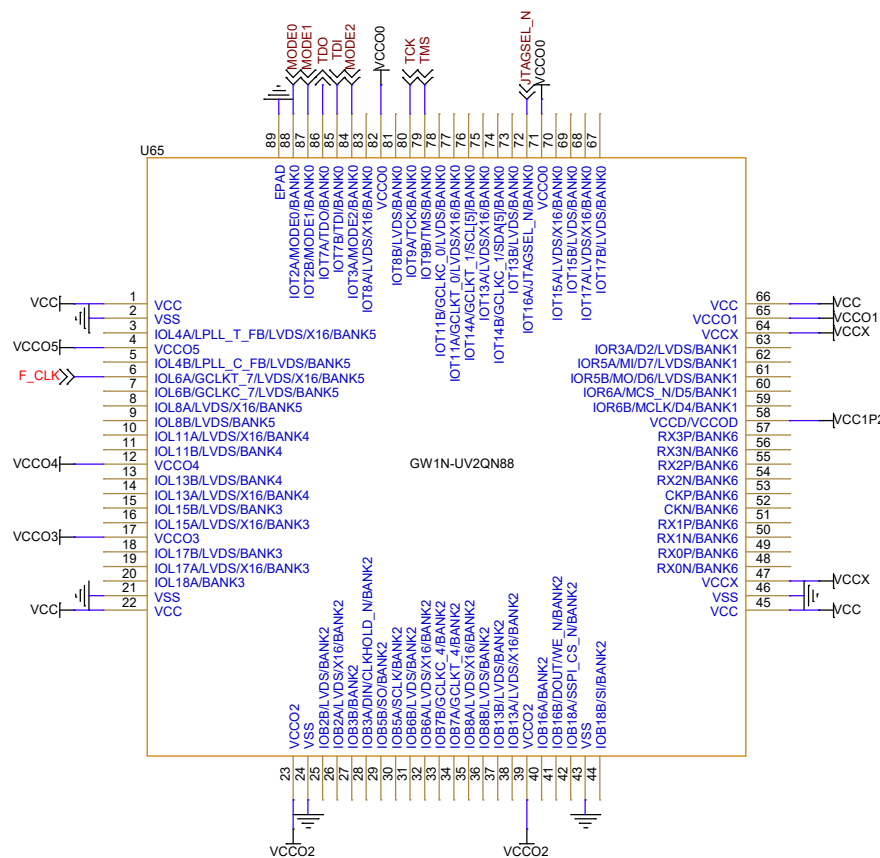
RECONFIG_N << R194 4.7K >> VCC00
*Configuration reset section

JTAGSEL_N << R195 1K >> VCC00
JTAG mode selection signal section

R196 1K >> MDOE0
*Configuration mode signal selection



Title		
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- Notes:
- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
 - 2.It is recommended that add an ESD protection chip to the JTAG download circuit.

Title		
GOWIN Minimum System SCH		
Size	Document Number	Rev
B	GW1N-UV2QN88	2.3
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